

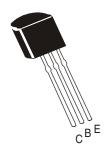
Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





NPN SILICON PLANAR EPITAXIAL TRANSISTORS



BC546, A, B, C BC547, A. B, C BC548, A. B, C

TO-92
Plastic Package
For Lead Free Parts, Device
Part # will be Prefixed with
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Amplifier Transistors

ABSOLUTE MAXIMUM RATINGS (T_a=25°C)

DESCRIPTION	SYMBOL	BC546	BC547	BC548	UNITS		
Collector Emitter Voltage	V _{CEO}	65	45	30	V		
Collector Emitter Voltage	V _{CES}	80	50	30	V		
Collector Base Voltage	V _{CBO}	80	50	30	V		
Emitter Base Voltage	V _{EBO}	6	6	5	V		
Collector Current Continuous	I _C	100					
Collector Current Peak	I _{CM}	200					
Base Current Peak	I _{BM}	200					
Emitter Current Peak	I _{EM}	200					
Power Dissipation at T _a =25°C	P _D	500					
Derate Above 25°C		4.0					
Storage Temperature	T _{stg}	- 65 to +150					
Junction Temperature	T _j	150					

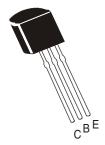
THERMAL RESISTANCE

Junction to Ambient in free air	R _{th (i-a)}	250	^o C/W
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ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	BC546	BC547	BC548	UNITS
Collector Emitter Voltage	V_{CEO}	I _C =1mA, I _B =0	>65	>45	>30	V
Collector Base Voltage	V _{CBO}	$I_{C}=10\mu A,\ I_{E}=0$	>80	>50	>30	V
Emitter Base Voltage	V_{EBO}	$I_{E}=10\mu A, I_{C}=0$	>6.0	>6.0	>5.0	V
Collector Cut Off Current	I _{CBO}	$V_{CB}=30V$, $I_{E}=0$ ALL		<50		nA
		$V_{CB}{=}30V,\ I_{E}{=}0,\ Tj{=}150^{\circ}C \textbf{ALL}$		< 5.0		μΑ
Collector Cut Off Current	I _{CES}	$V_{CE}=80V$, $V_{BE}=0$	<15			nA
		V_{CE} =50V, V_{BE} =0		<15		nA
		V_{CE} =30V, V_{BE} =0			<15	nA
		$V_{CE}=80V, V_{BE}=0, T_{j}=125^{\circ}C$	<4.0			μΑ
		$V_{CE}=50V, V_{BE}=0, T_{j}=125^{\circ}C$		<4.0		μΑ
		$V_{CE}=30V, V_{BE}=0, T_{j}=125^{\circ}C$			<4.0	μΑ

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ELECTRICAL CHARACTERISTICS (T_a=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DC Current Gain	h _{FE}	$I_C=10\mu A,\ V_{CE}=5V$				
		Α		90		
		В		150		
		С		270		
		$I_C=2mA, V_{CE}=5V$				
		BC546	110		450	
		BC547/548	110		800	
		Α	110		220	
		В	200		450	
		С	420		800	
		$I_C=100$ mA, $V_{CE}=5$ V				
		A		120		
		В		200		
		С		400		
Collector Emitter Saturation Voltage	V _{CE (sat)}	$I_C=10$ mA, $I_B=0.5$ mA			0.25	V
		$I_C=100$ mA, $I_B=5$ mA			0.60	V
Base Emitter Saturation Voltage	V _{BE (sat)}	$I_C=10$ mA, $I_B=0.5$ mA		0.7		V
		$I_C=100$ mA, $I_B=5$ mA		0.9		V
Base Emitter On Voltage	V _{BE (on)}	$I_C=2mA$, $V_{CE}=5V$	0.55		0.70	V
		I _C =10mA, V _{CE} =5V			0.77	V

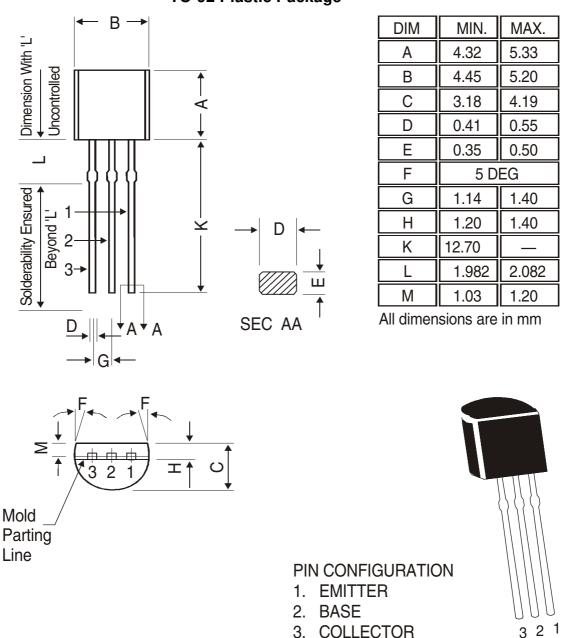
SMALL SIGNAL CHARACTERISTICS

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Transistors Frequency	f _T	$I_C=10$ mA, $V_{CE}=5$ V, $f=100$ MHz		300		MHz
Collector Output Capacitance	C_{cbo}	V _{CB} =10V, f=1MHz			4.5	pF
Emitter Input Capacitance	C_{ib}	V_{EB} =0.5V, f=1MHz		9.0		рF
Noise Figure	NF	I_C =0.2mA, V_{CE} =5V, R_S =1 k Ω , f=1KHz, B=200Hz			10	dB
Small Signal Current Gain	h _{fe}	$I_C=2mA$, $V_{CE}=5V$, $f=1KHz$				
		A B C		220 330 600		
Input Impedance	h _{ie}	I _C =2mA, V _{CE} =5V, f=1KHz				
		A B C	1.6 3.2 6.0		4.5 8.5 15	kΩ kΩ kΩ
Voltage Feedback Ratio	h _{re}	I _C =2mA, V _{CE} =5V, f=1KHz				
		A B C		1.5 2.0 3.0		x10 ⁻⁴ x10 ⁻⁴ x10 ⁻⁴
Out Put Admittance	h _{oe}	$I_C=2mA$, $V_{CE}=5V$, $f=1KHz$				
		Α			30	umhos
		В			60	umhos
		С			110	umhos

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"T"

TO-92 Plastic Package



The TO-92 Package, Tape and Ammo Pack drawings are correct as on the date of issue/revision of this Data Sheet.

The currently valid dimensions and information, may please be confirmed from the TO-92 Drawing in the Packages and Packing Section of the Product Catalogue.

Packing Details

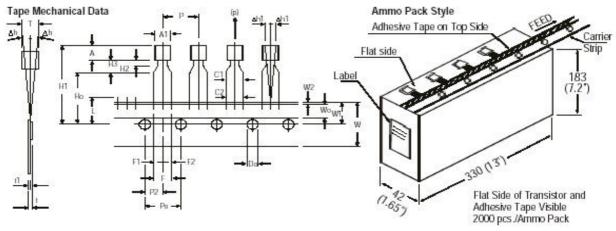
PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/ Oty	Size Oty :		Size Qty		Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

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TO-92 Plastic Package

For Lead Free Parts, Device Part # will be Prefixed with "T"

TO-92 Tape and Ammo Pack



All dimensions are in mm

		SPECIFICATION			ON	
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	1
BODY WIDTH	A1	4.45		5.20	ř.	NOTES
BODY HEIGHT	Α	4.32		5.33		Maximum alignment deviation between
BODY THICKNESS	T	3.18		4.19		leads will not to be greater than 0.2mm
PITCH OF COMPONENT	P		12.7		± 1.0	Maximum non-cumulative variation
*1FEED HOLE PITCH	Po		12.7		± 0.3	between tape feed holes shall not
*2 FEED HOLE CENTRE TO	50000		19882000			exceed 1 mm in 20 pitches.
COMPONENT CENTRE	P2		6.35		± 0.4	3. Holddown tape will not exceed beyond
DISTANCE BETWEEN OUTER LEADS	F:		5.08		+ 0.6	the edge(s) of carrier tape and there shall be no exposure of adhesive.
*3 COMPONENT ALIGNMENT SIDE VIEW	Δh		0	1.0		4. There will be no more than three (3)
*4 COMPONENT ALIGNMENT FRONT VIEW	Δh1		0	1.3		consecutive missing components in a
TAPE WIDTH	W		18	chocked	± 0.5	tape.
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	A tape trailer, having at least three feed
HOLE POSITION	W1		9		+ 0.7	holes are provided after the last component in a tape.
	200000000000000000000000000000000000000	-1-0-7-0-0-7-1			- 0.5	- [- 1] 가게 하는 이 이 사람이 이 사람들이 아니었습니다. 그렇게 다 되었습니다.
HOLD-DOWN TAPE POSITION	W2	0.0		0.7		Splices should not interfere with the sprocket feed holes.
LEAD WIRE CLINCH HEIGHT	Ho		16		± 0.5	Sprocket read notes.
COMPONENT HEIGHT	H1			24.0		
LENGTH OF SNIPPED LEADS	L		89	11.0	- 2523	
FEED HOLE DIAMETER	Do		4		± 0.2	REMARKS
*5 TOTAL TAPE THICKNESS	t			1.2		*1 Cumulative pitch error 1.0 mm/20 pitch
LEAD - TO - LEAD DISTANCE	F1, F2	2.40		2.70	- 0.1	*2 To be measured at bottom of clinch
STAND OFF	H2	0.45		1.45	0.1	
CLINCH HEIGHT	Н3			3.0		*3 At top of body
LEAD PARALLELISM	C1 - C2			0.22		*4 At top of body
PULL - OUT FORCE	(p)	6N				*5 t1 0.3 – 0.6 mm

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Customer Notes

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Continental Device India Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119
email@cdil.com www.cdilsemi.com