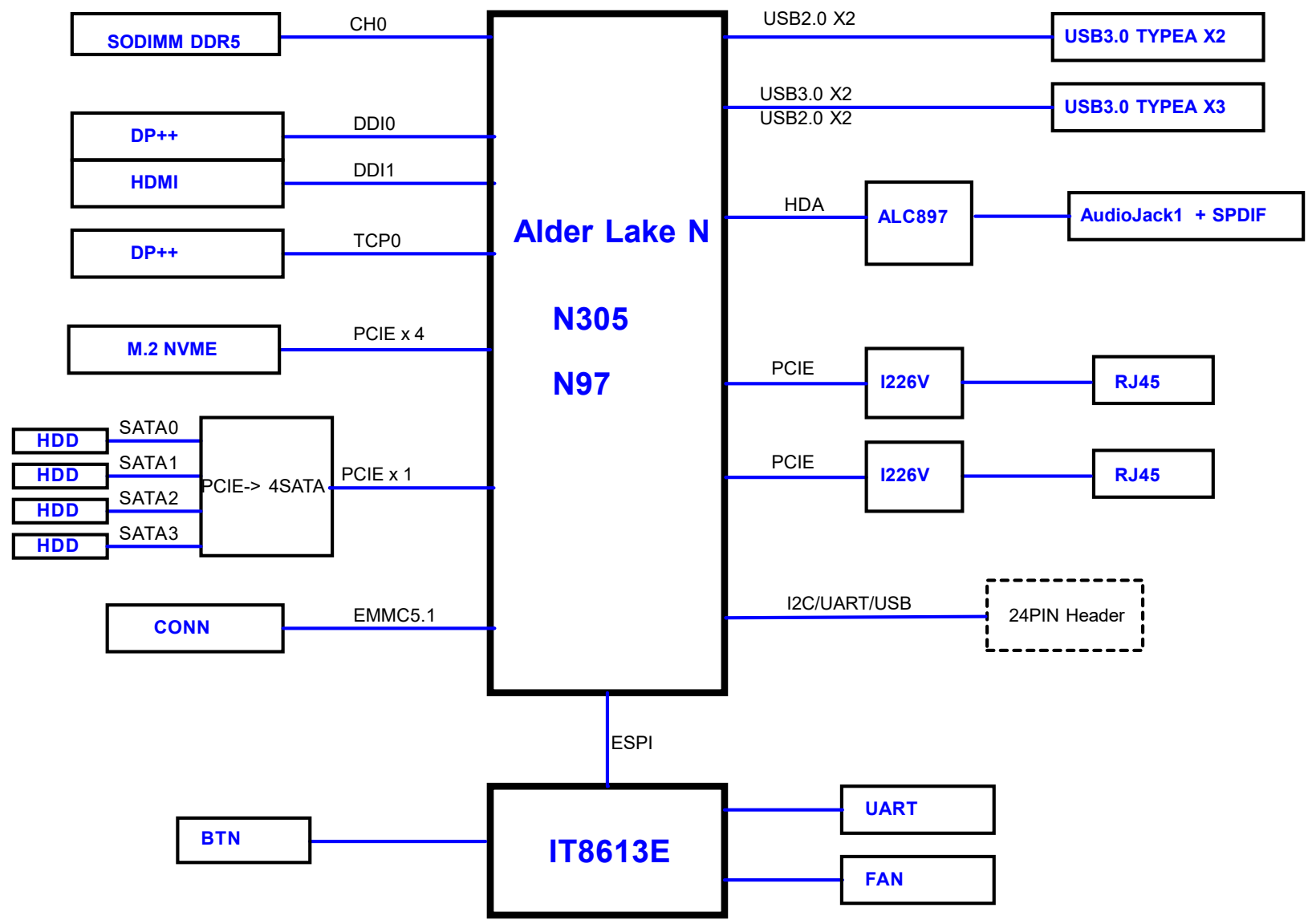


ODROID-H4 MB Schematics

Intel Alder Lake N Platform

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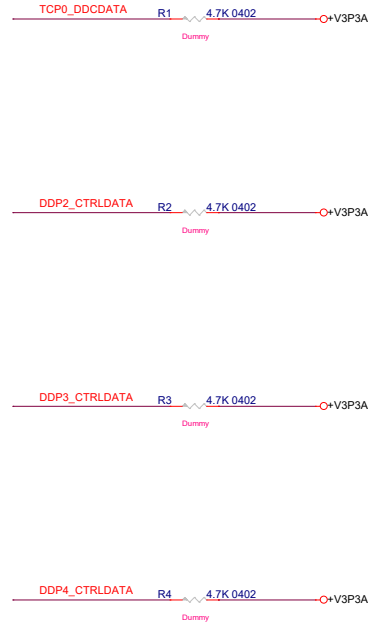
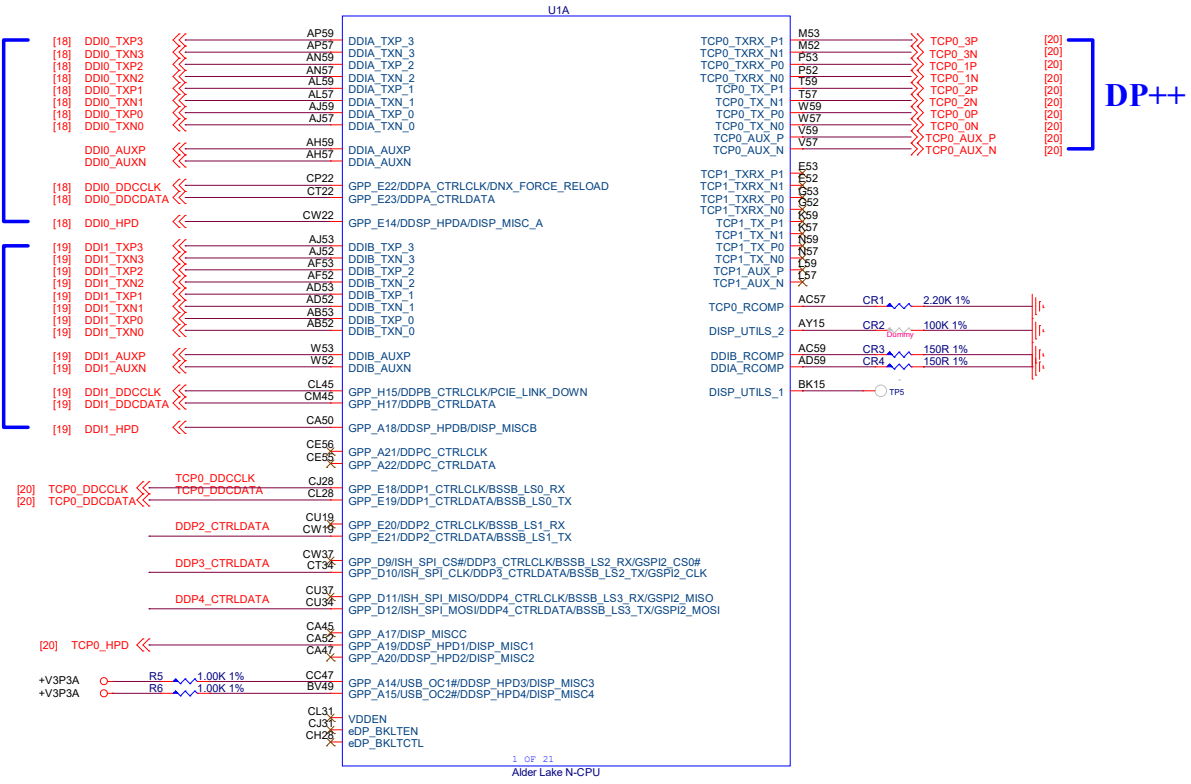
Table of Content

SCHEMATIC1*	
01 -- Cover	21 = LAN1(I226-V)
02 -- System Block*	22 = LAN2(I226-V)
03 -- Table of Content	23 = M.2 PCIe SSD
04 -- CPU(DDI)	24 = IT8613E/LX
05 -- CPU(DDR)	25 = PWR SW/LED/FAN
06 -- CPU(SPI/ESPI/SMB/GPIO)	26 = PWR DC-IN
07 -- CPU(EMMC/PCIE)	27 = PWR(3P3A/5A)
08 -- CPU(CLK/PM)	28 = PWR(1P8A)
09 -- CPU(VCORE/VGT/VDD2)	29 = PWR(VCCIN_AUX)
10 -- CPU(VCCINAUX/1P8A)	30 = PWR(VCCST/VCCSTG)
11 -- CPU(GND)	31 = PWR(VDDR)
12 = SODIMM5	32 = PWR VCORE - 1
13 = HDA(ALC897)	33 = PWR VCORE - 2
14 = HDA CONN	34 = PWR System
15 = EMMC	35 -- PWR Glue-logic
16 = USB3 X 2 PORT	36 = PWR +12V_RUN
17 = USB2 X 2 PORT	37 = PCIE =>SATA(ASM1064)
18 = HDMI	38 = HDD & PWR
19 = DP++	39 = GPIO HEAD/UART/I2C
20 = DP++	40 ---- Change List

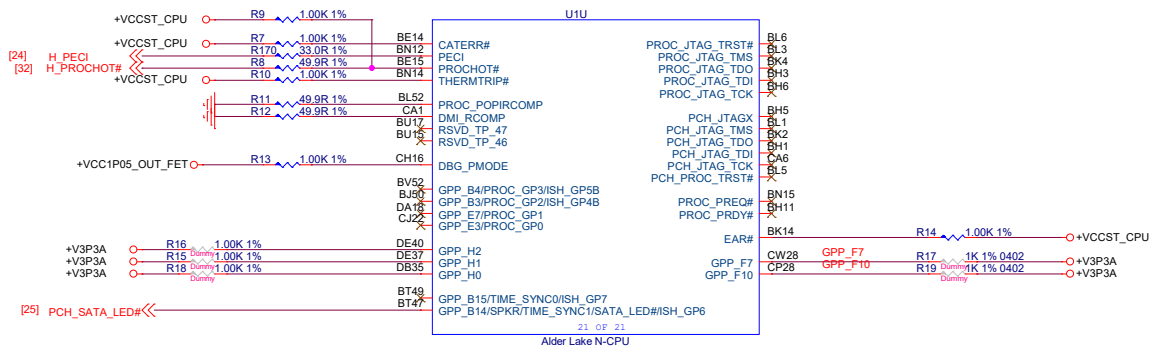
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HDMI

DP++



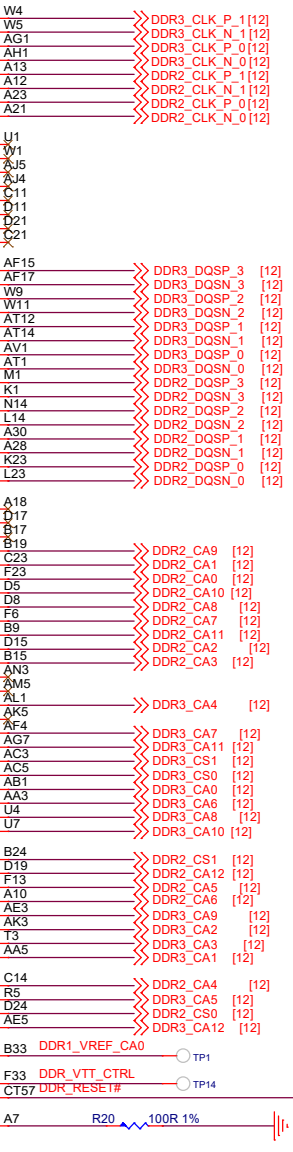
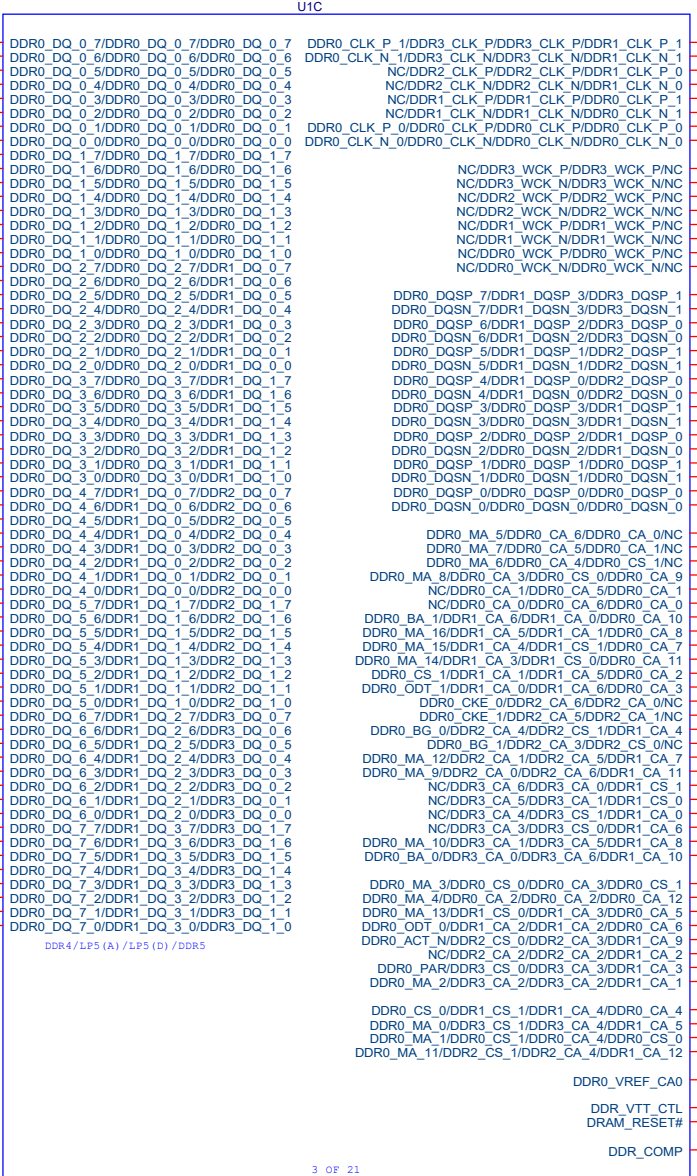
1 of 21 Alder Lake N-CPU



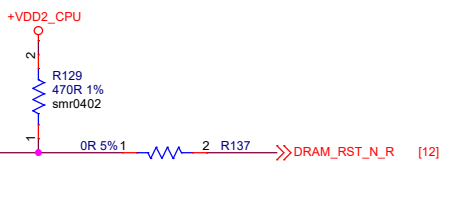
21 of 23 Alder Lake N-CPU

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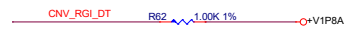
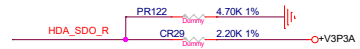
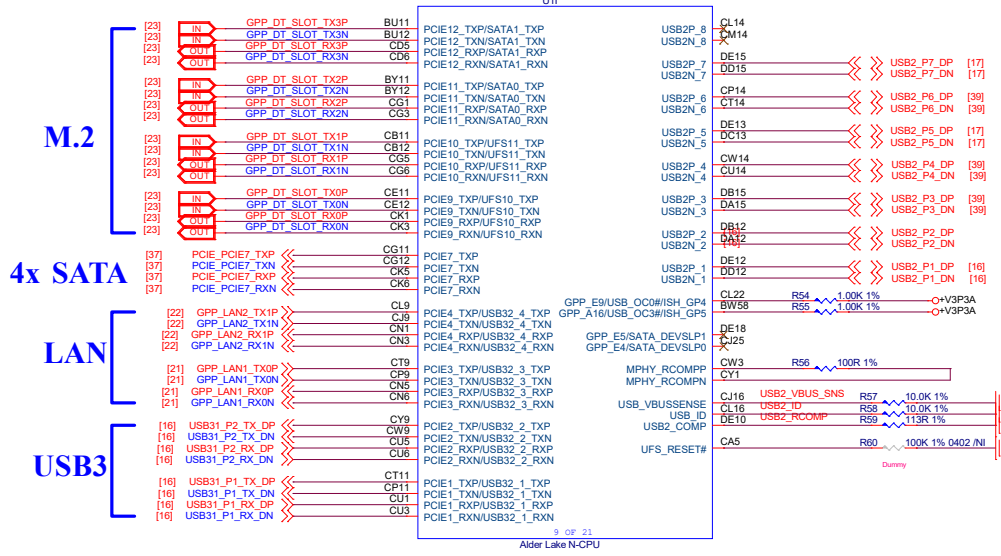
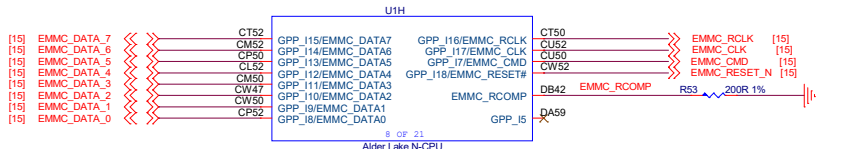
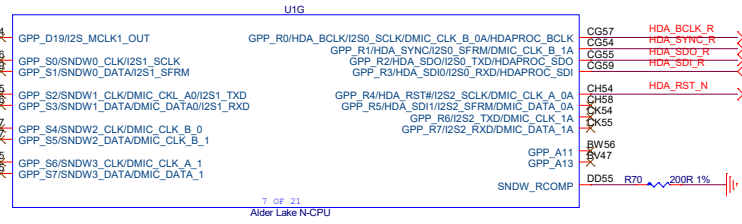
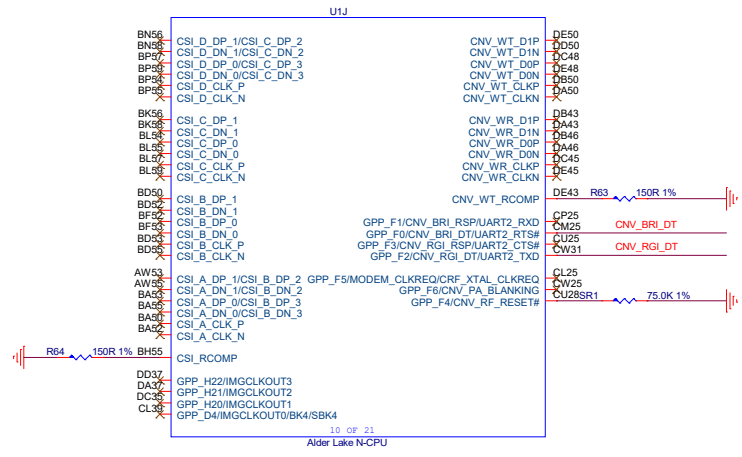
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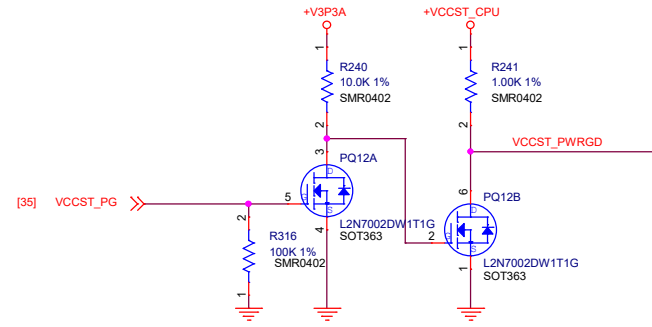
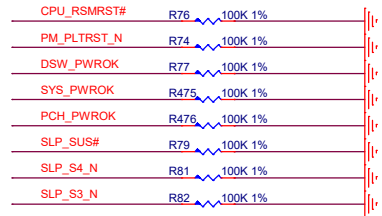
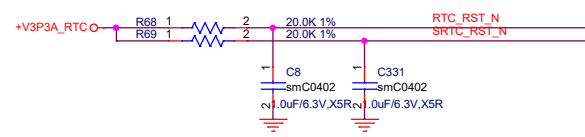
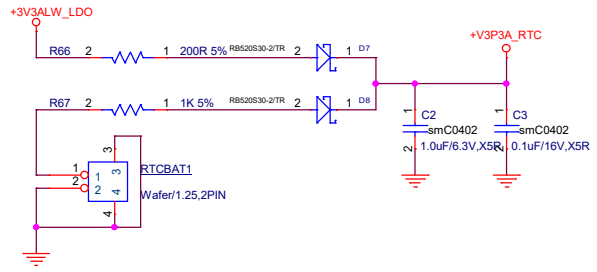
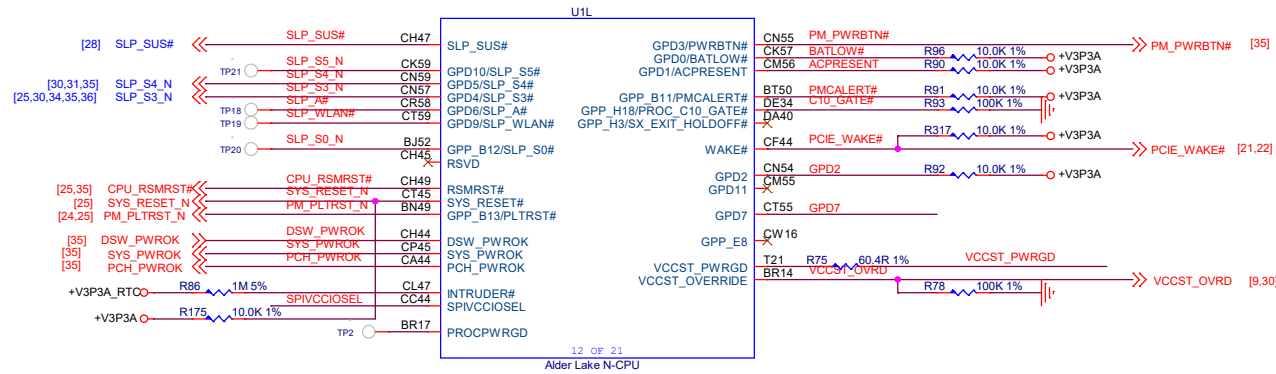
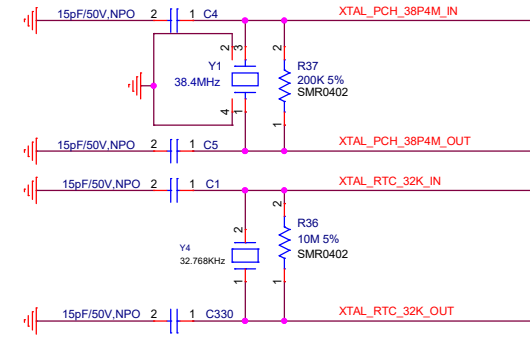
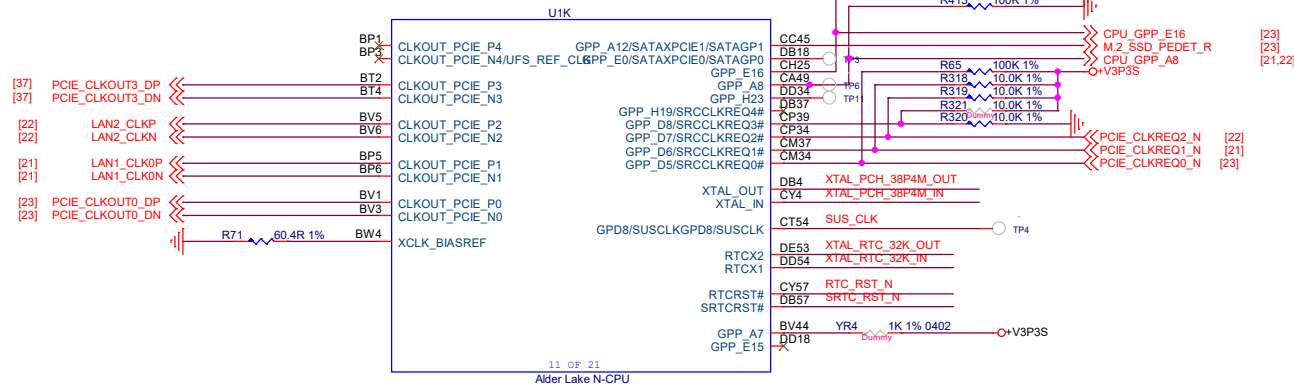
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 Alder Lake N-CPU



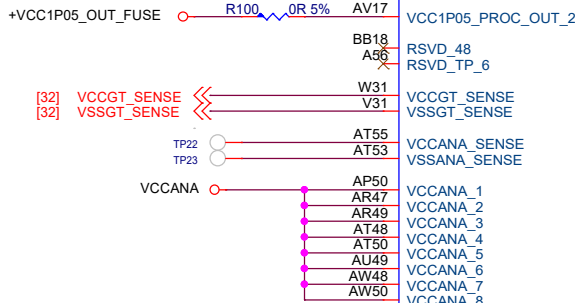
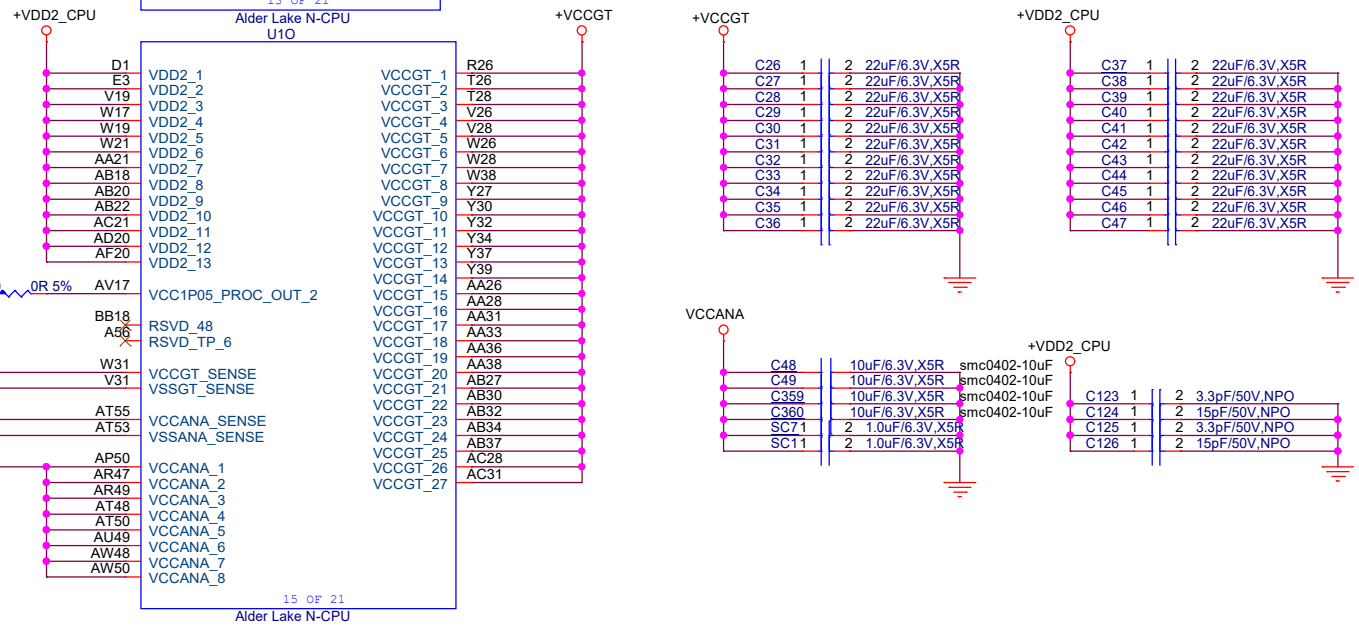
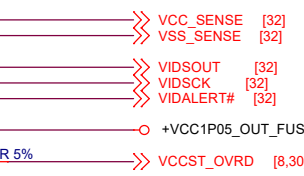
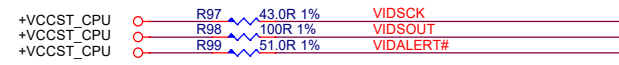
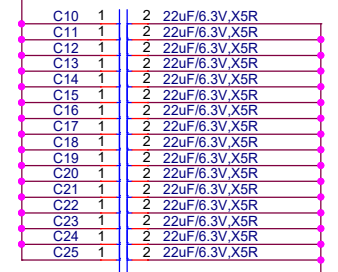
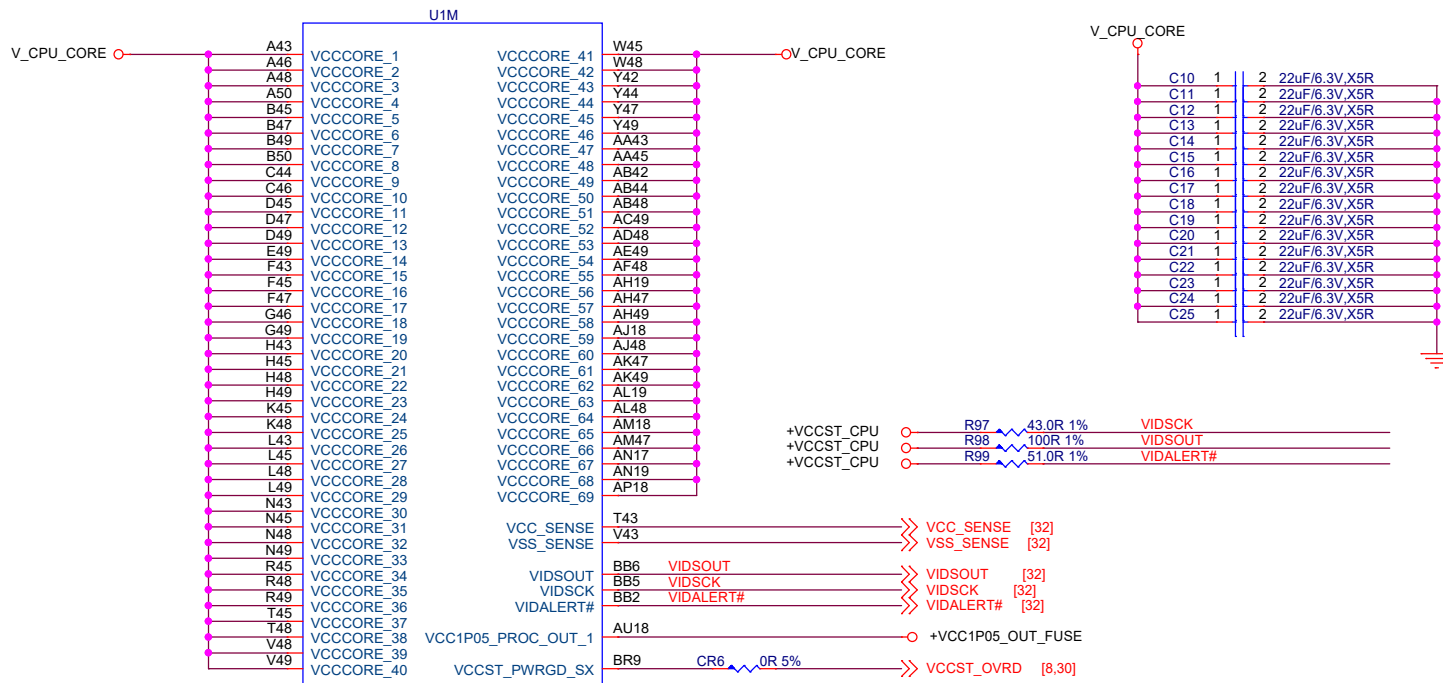
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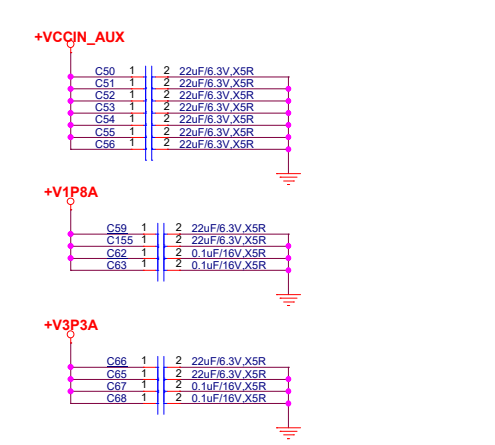
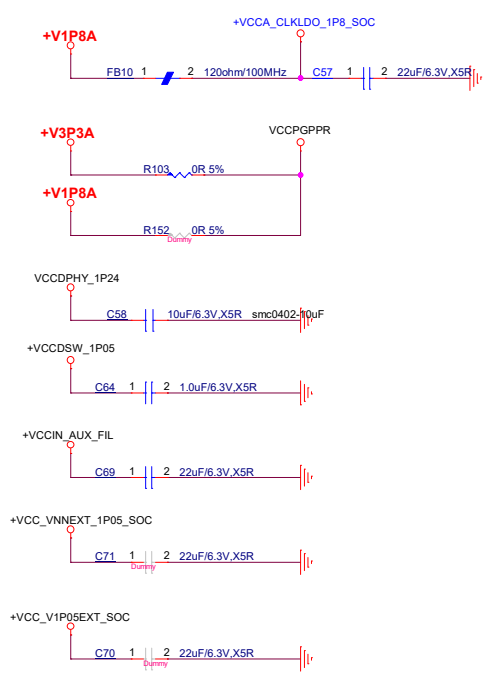
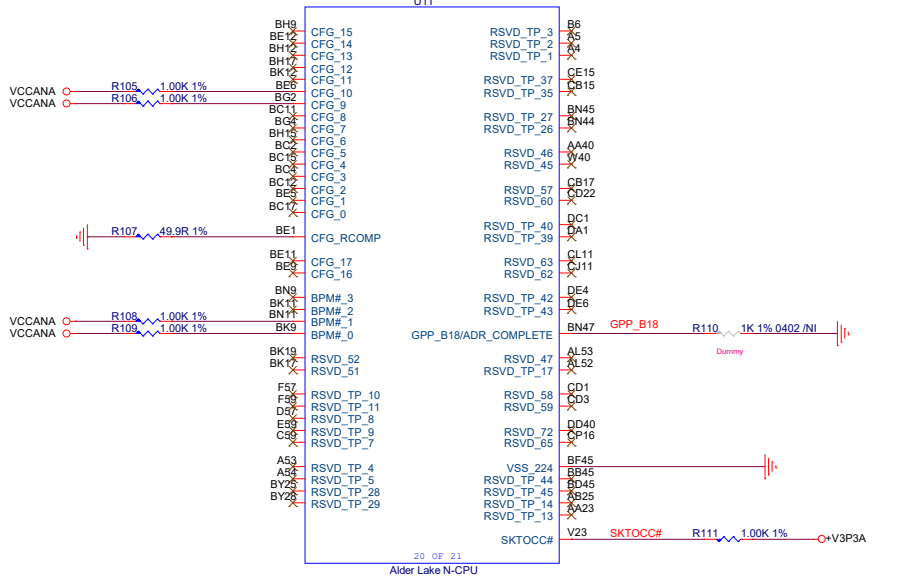
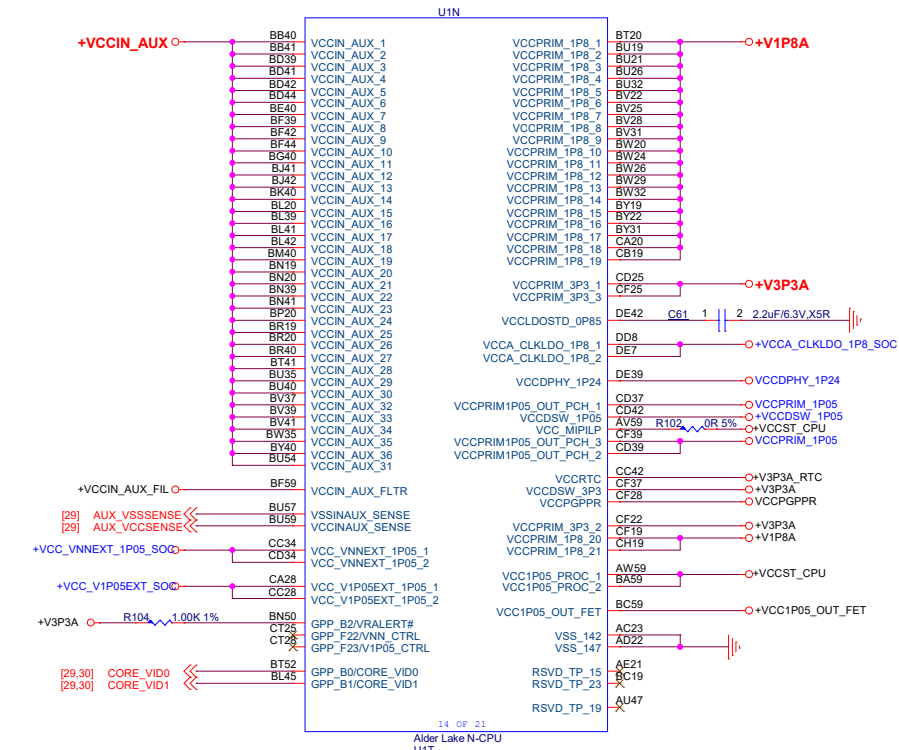
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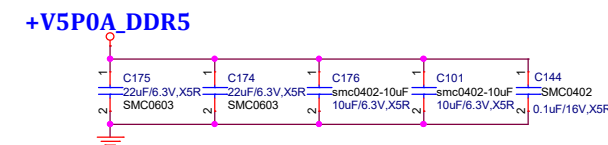
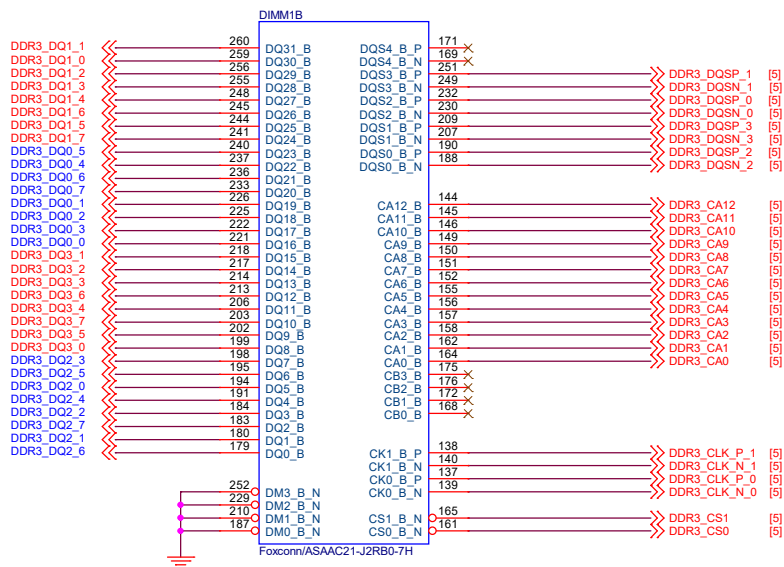
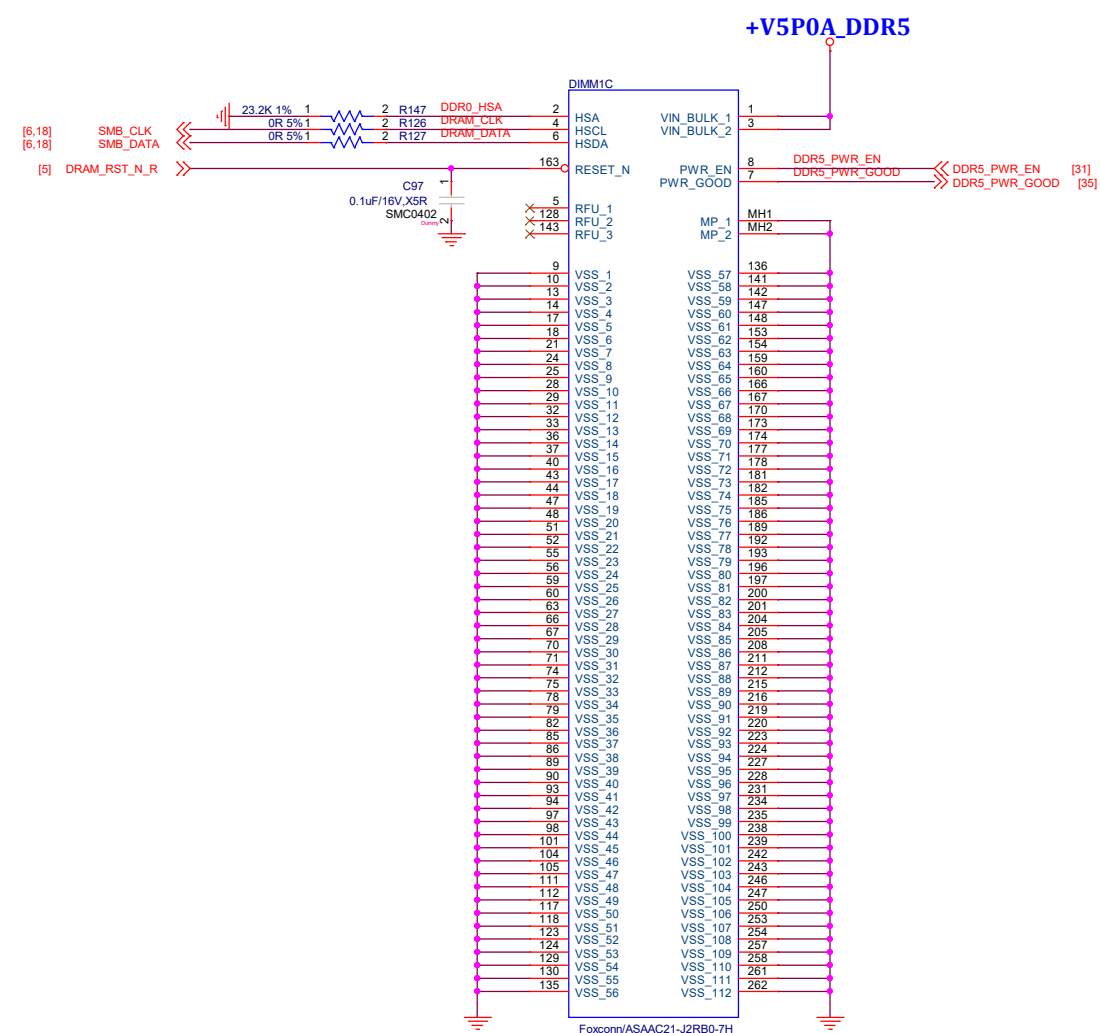
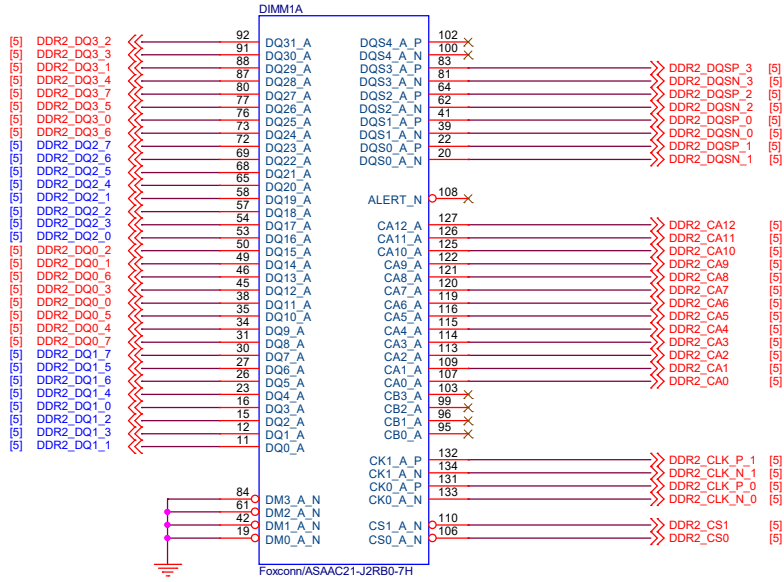
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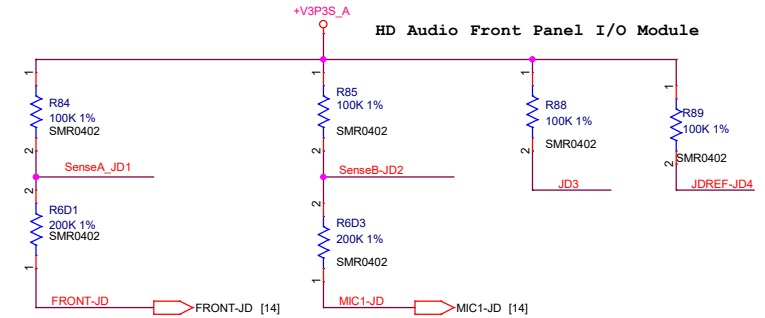
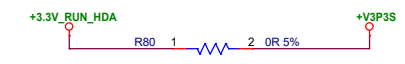
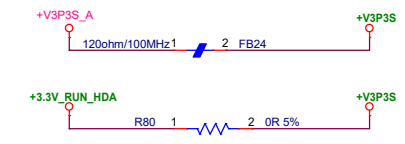
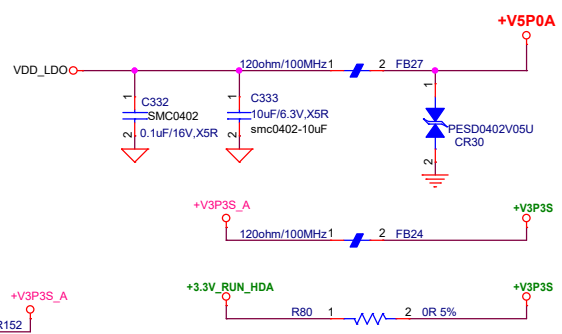
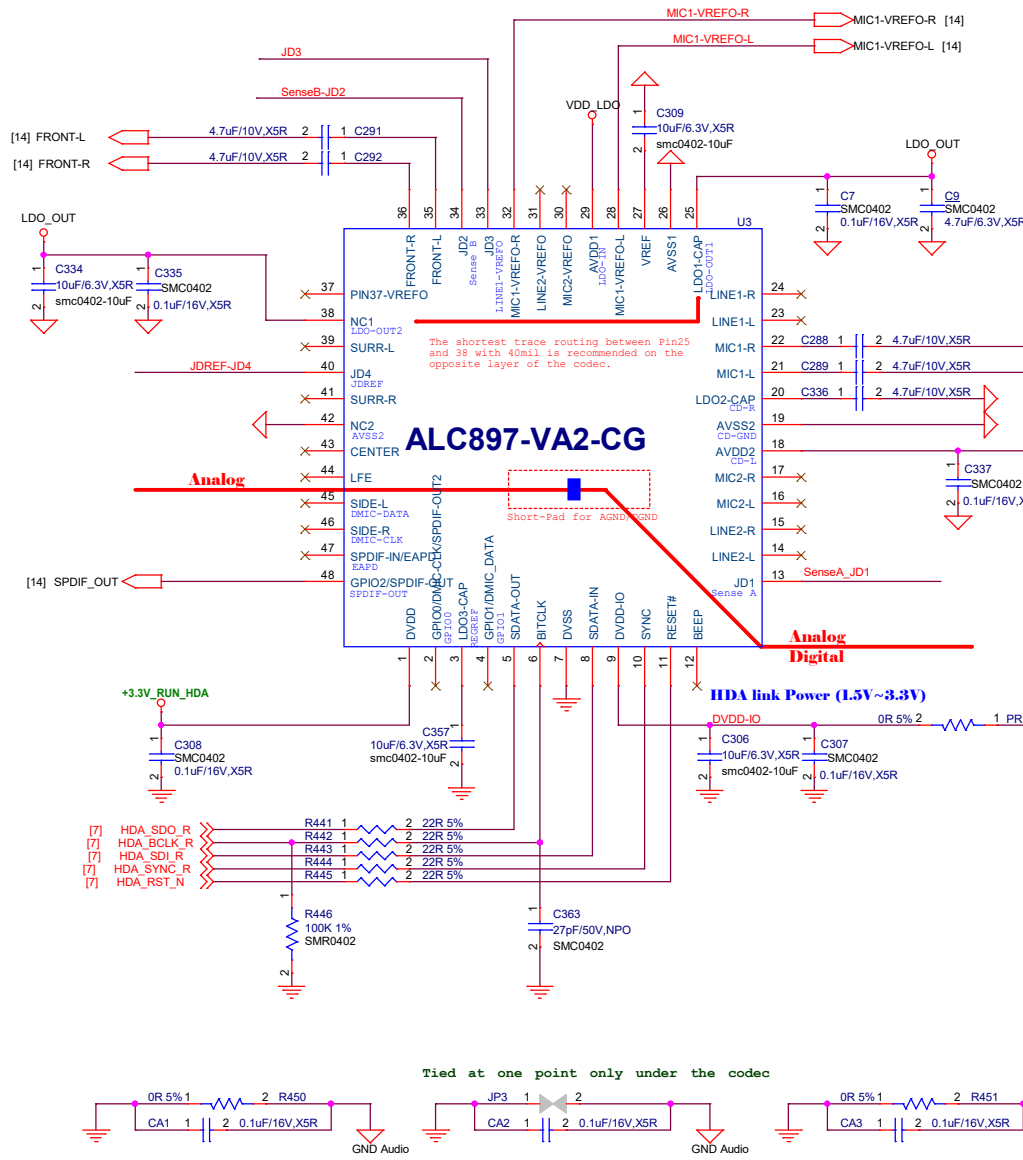
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DDR5 SODIMM CHANNEL A



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ALC897-VA2-CG

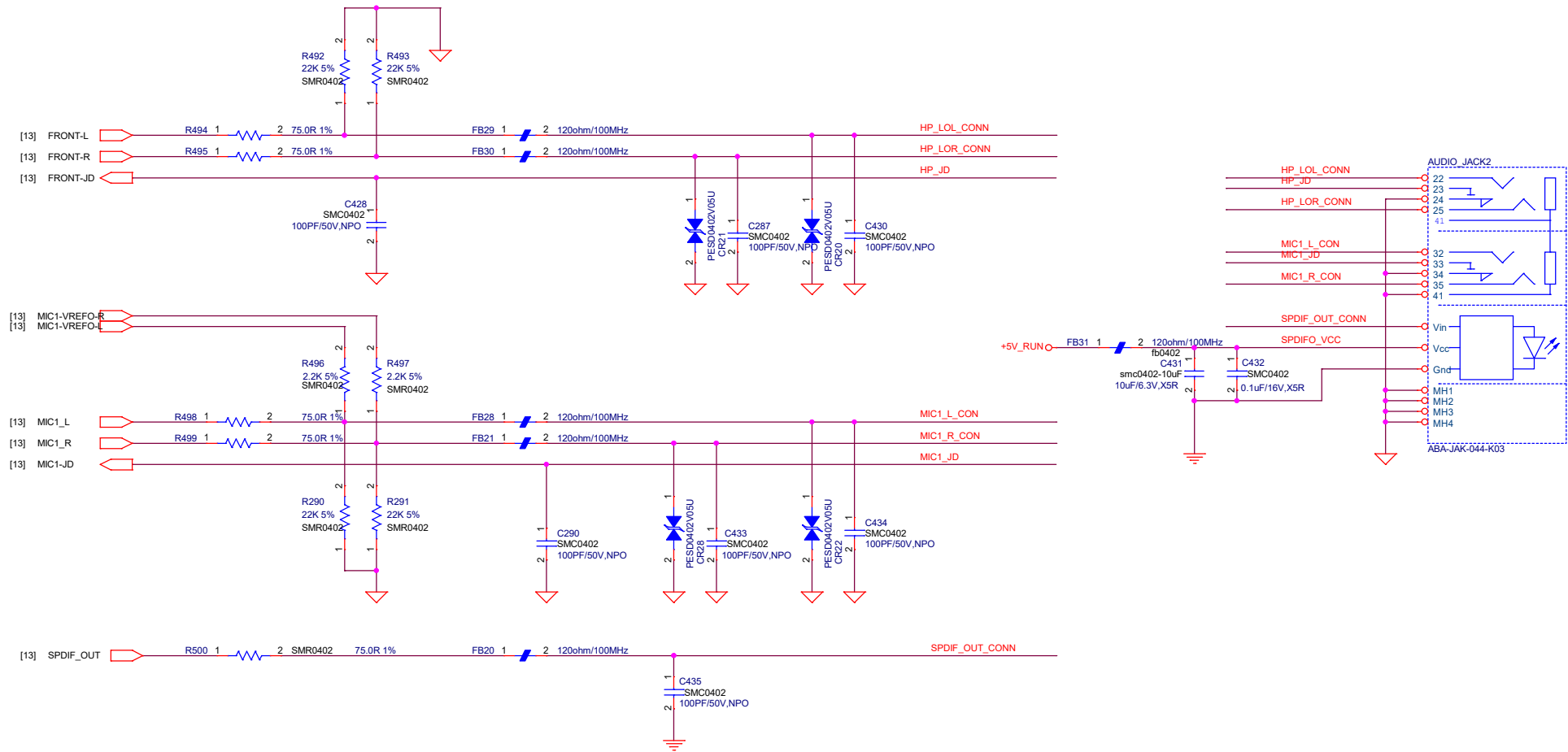
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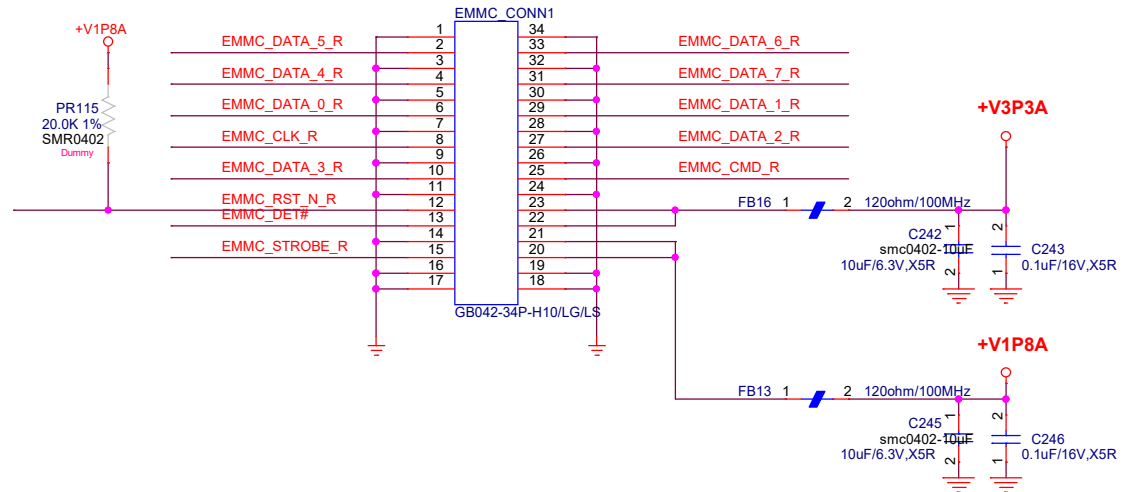
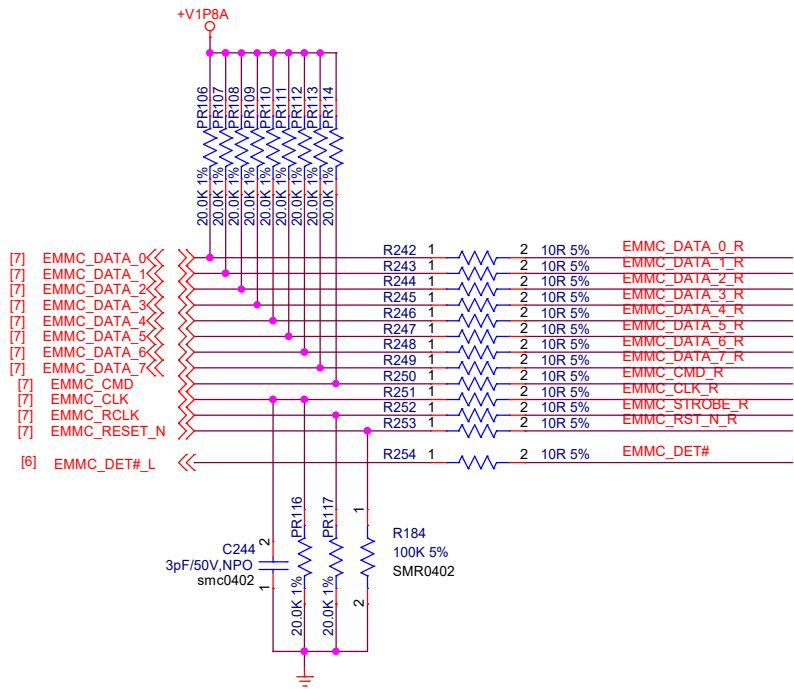
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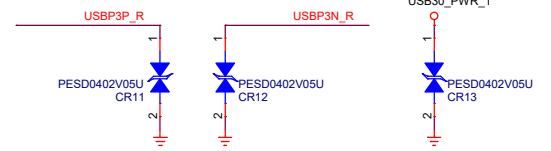
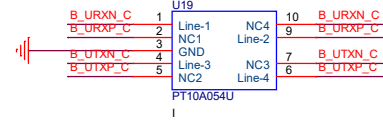
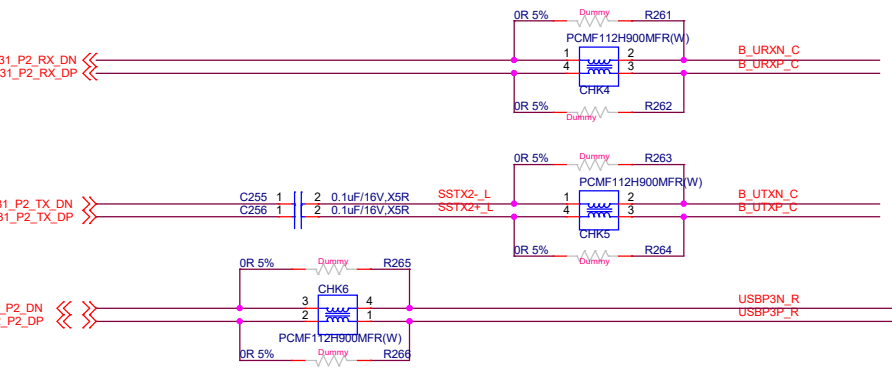
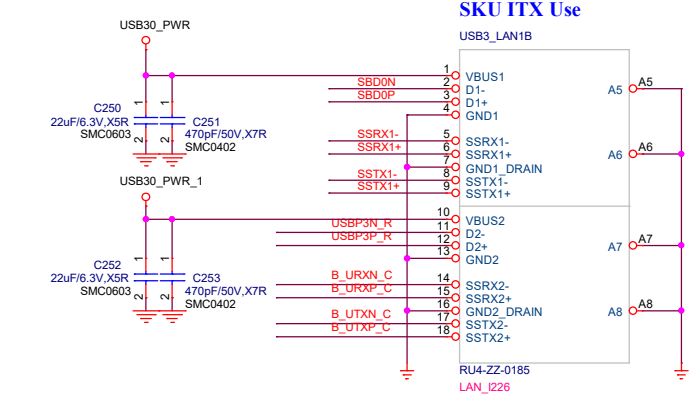
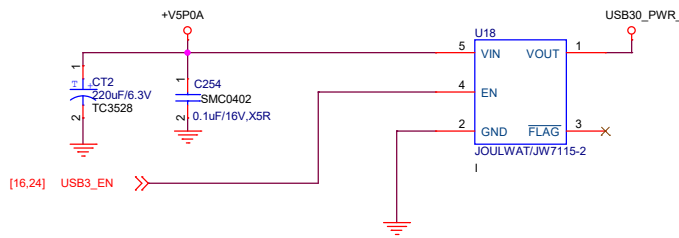
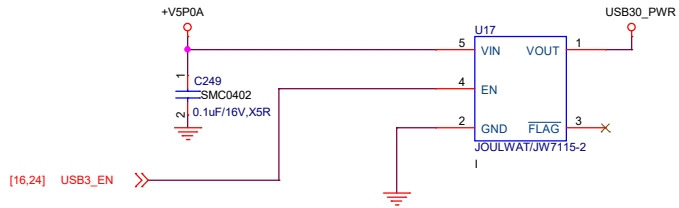
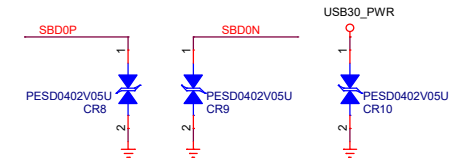
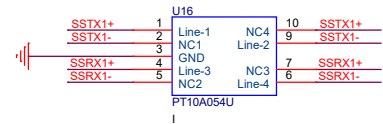
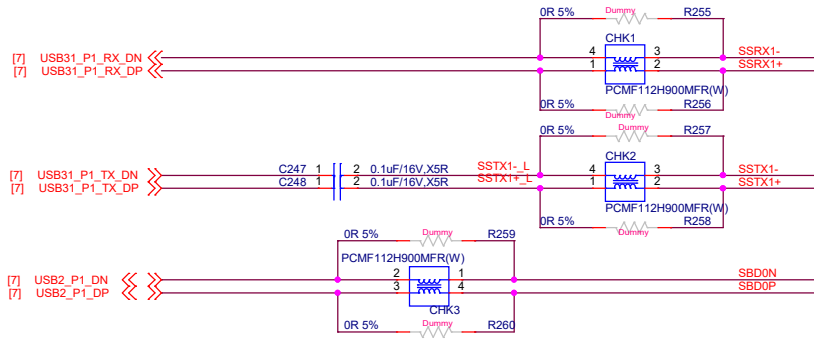
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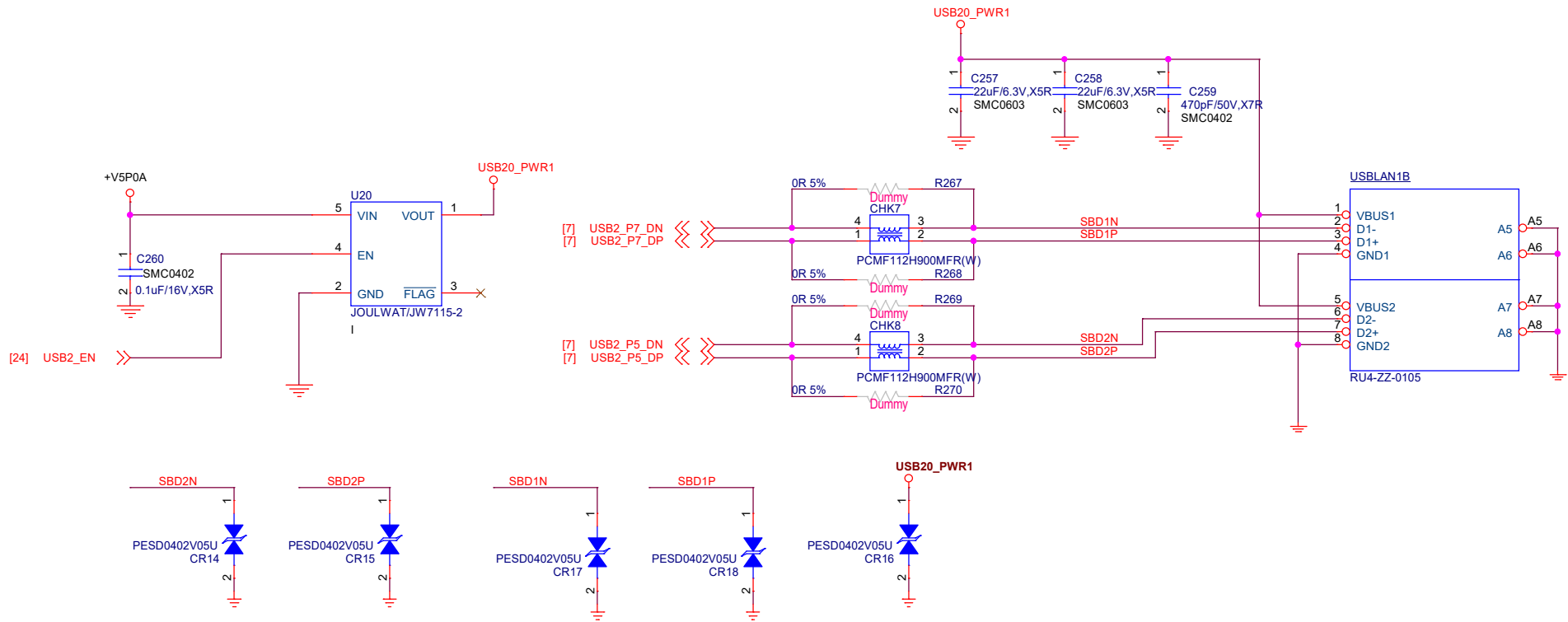


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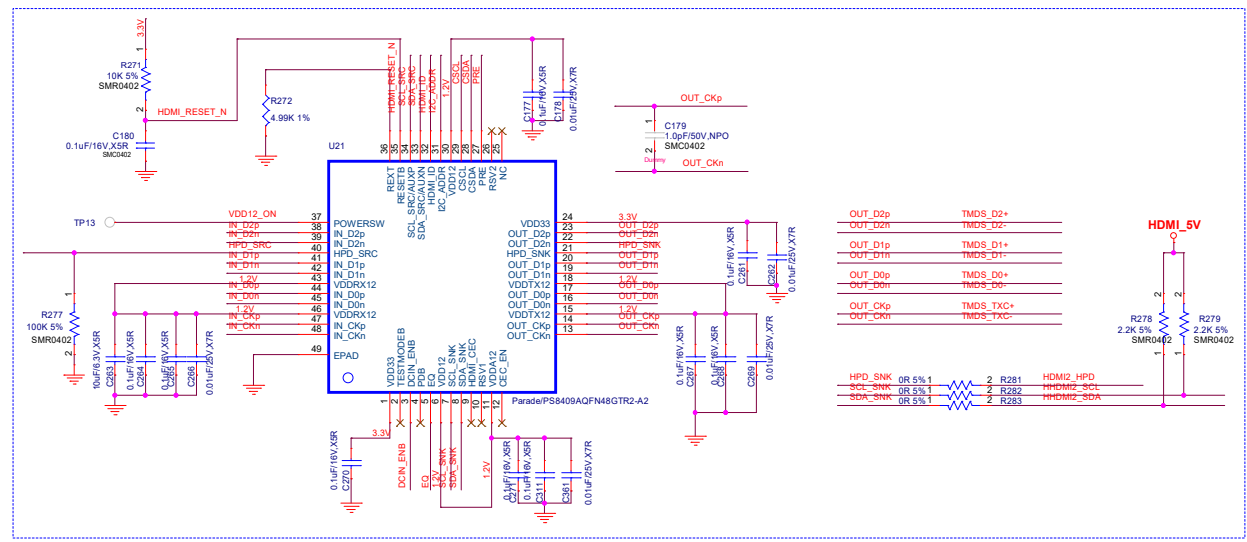
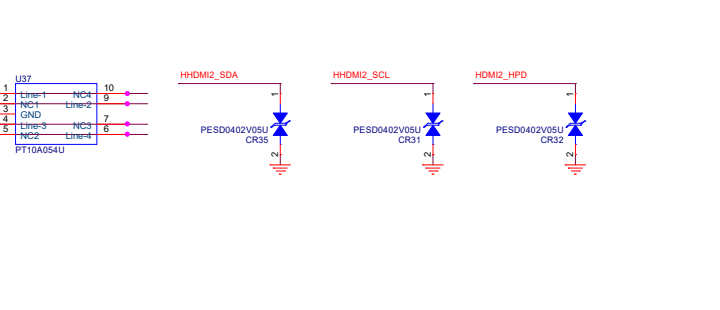
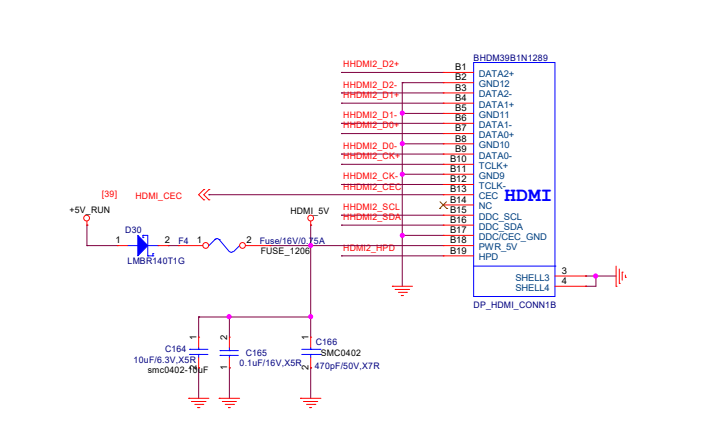
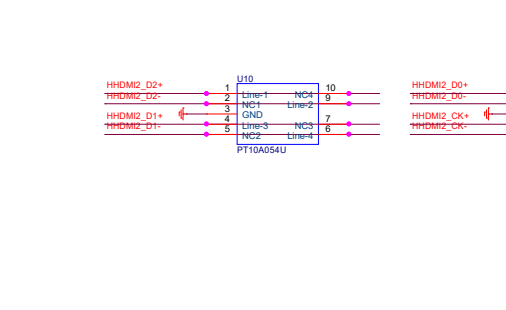
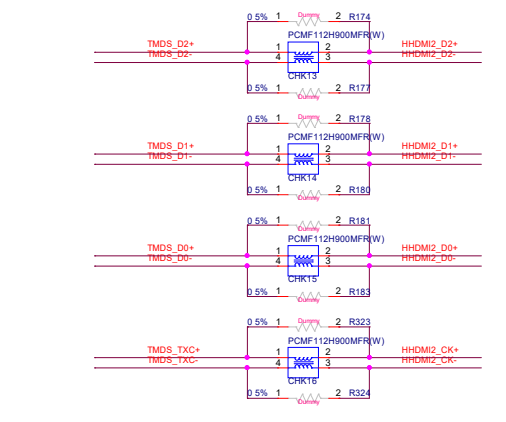
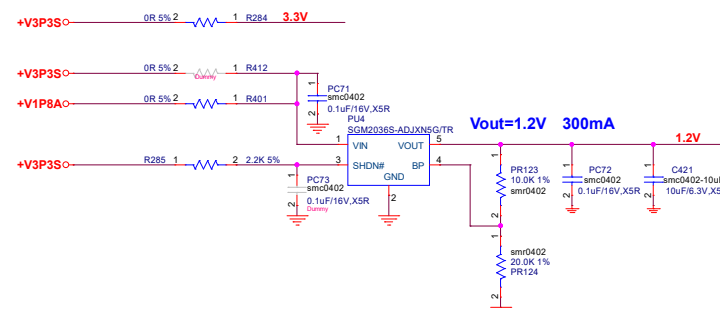
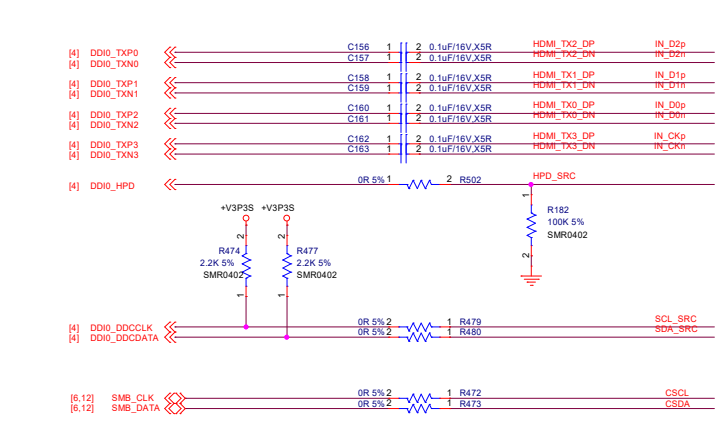


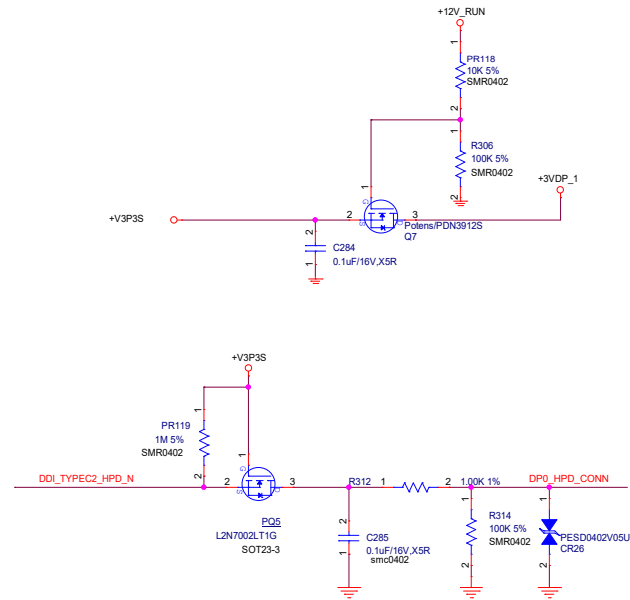
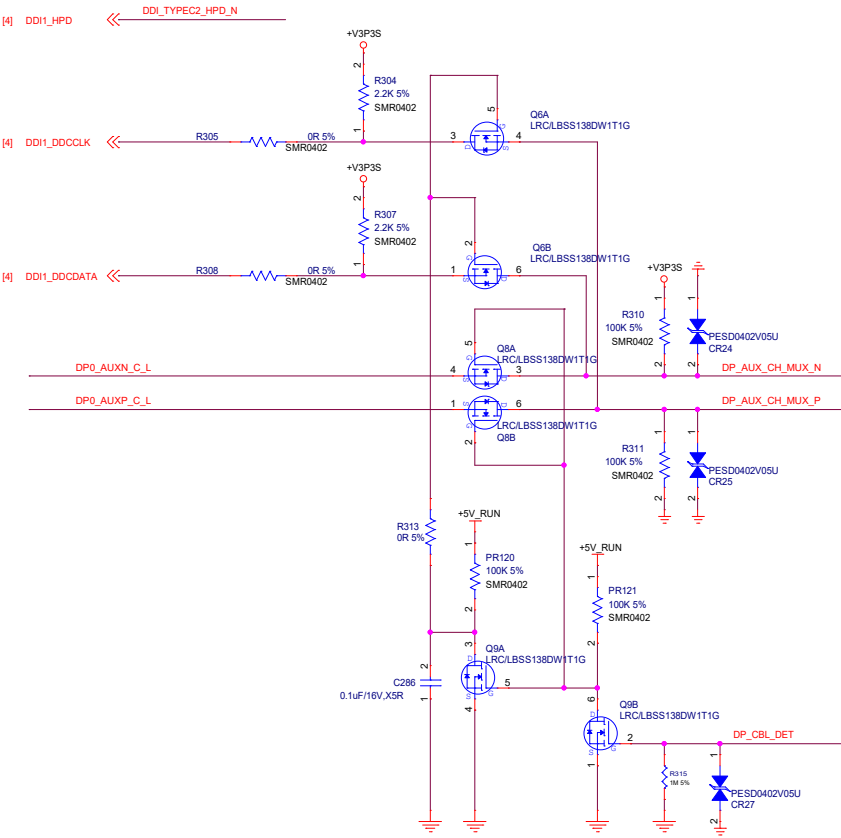
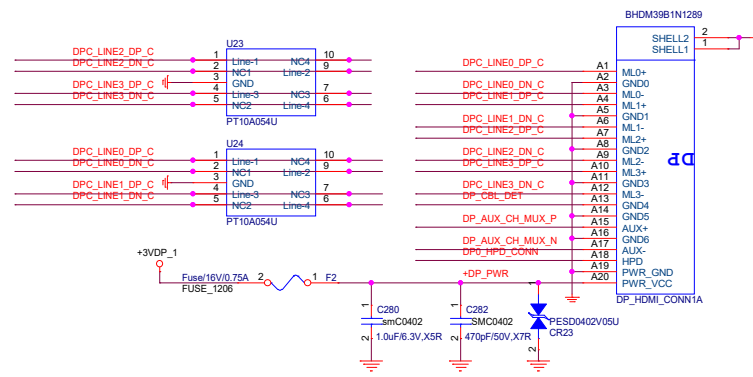
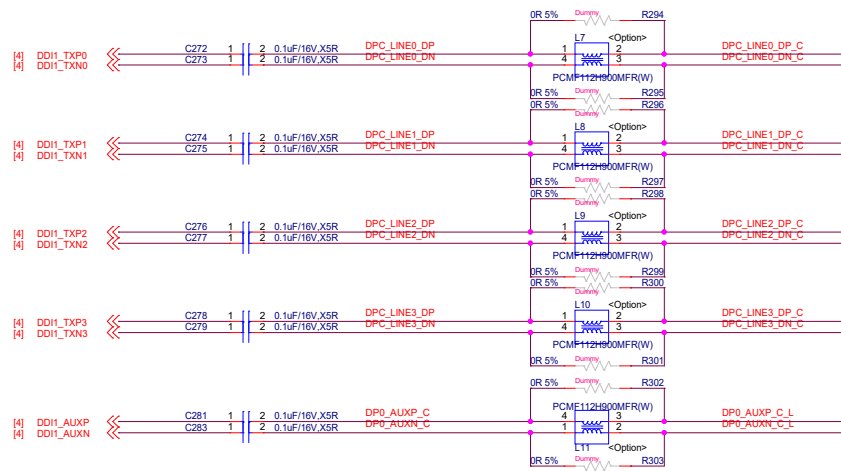


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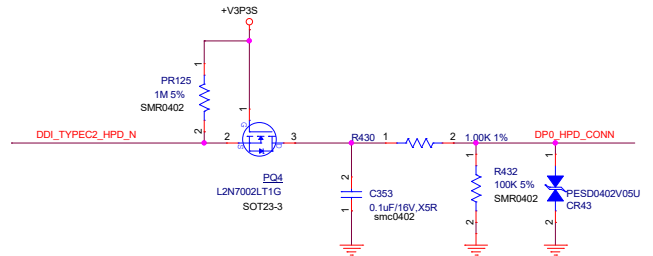
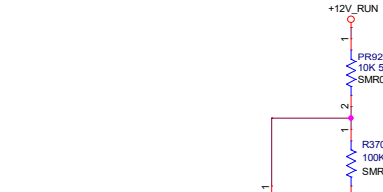
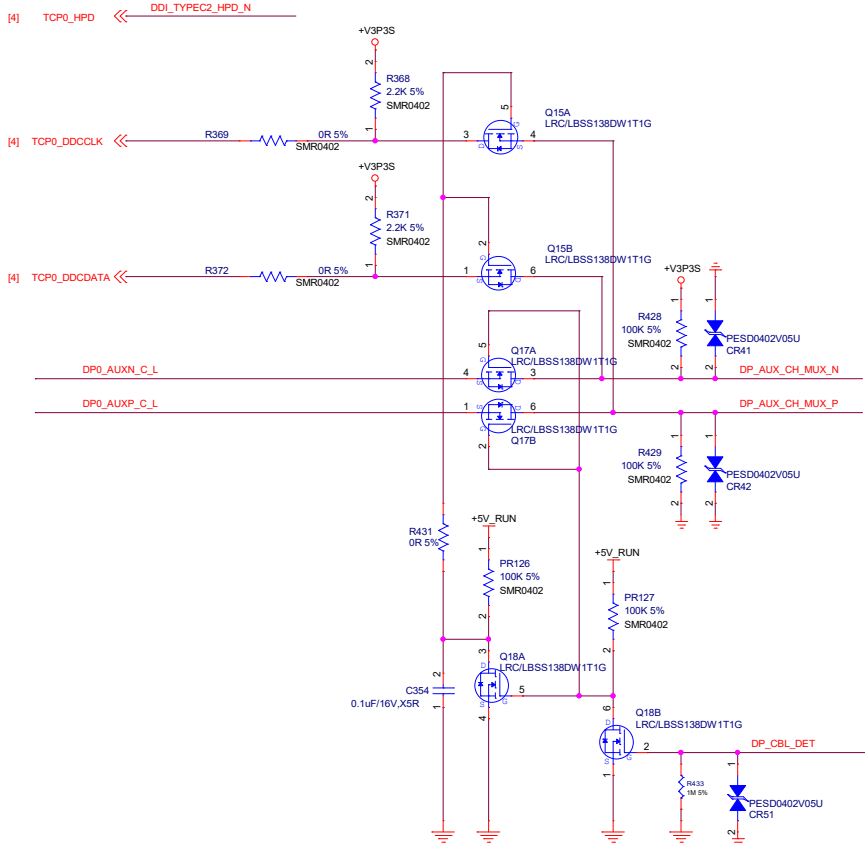
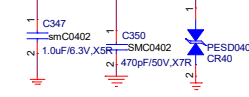
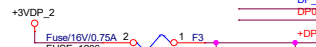
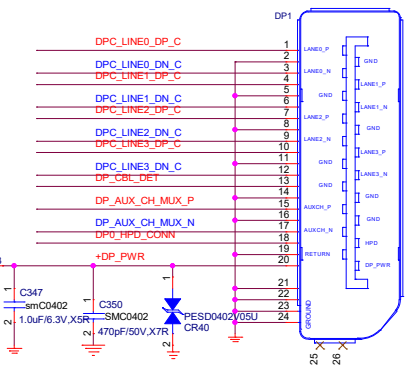
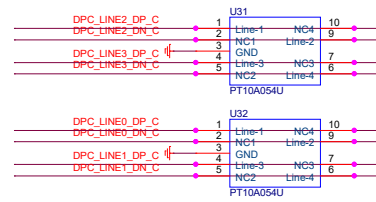
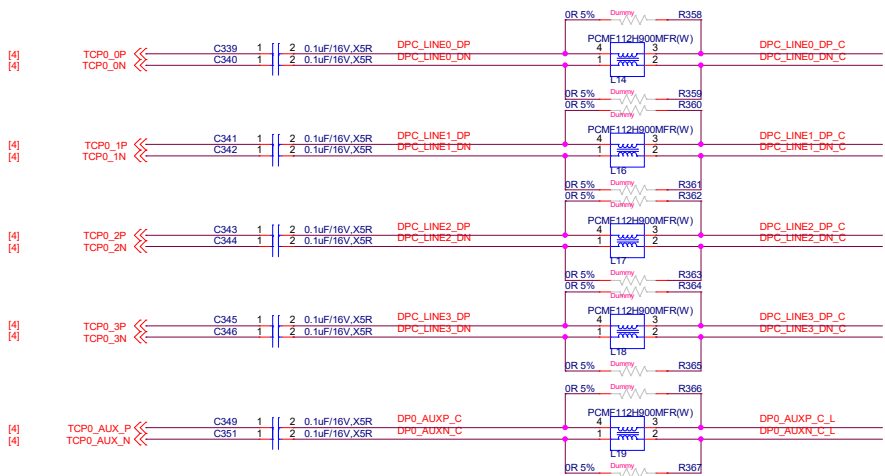


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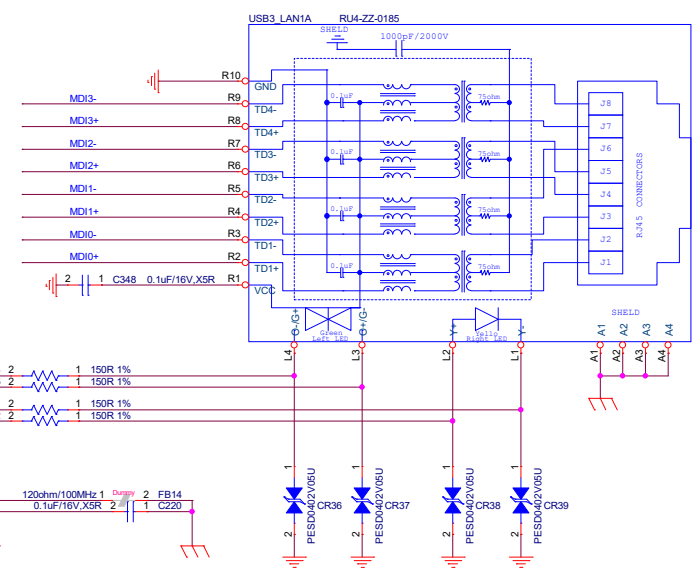
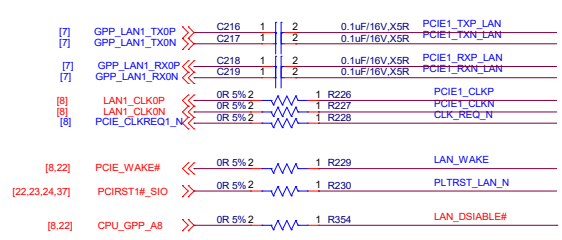
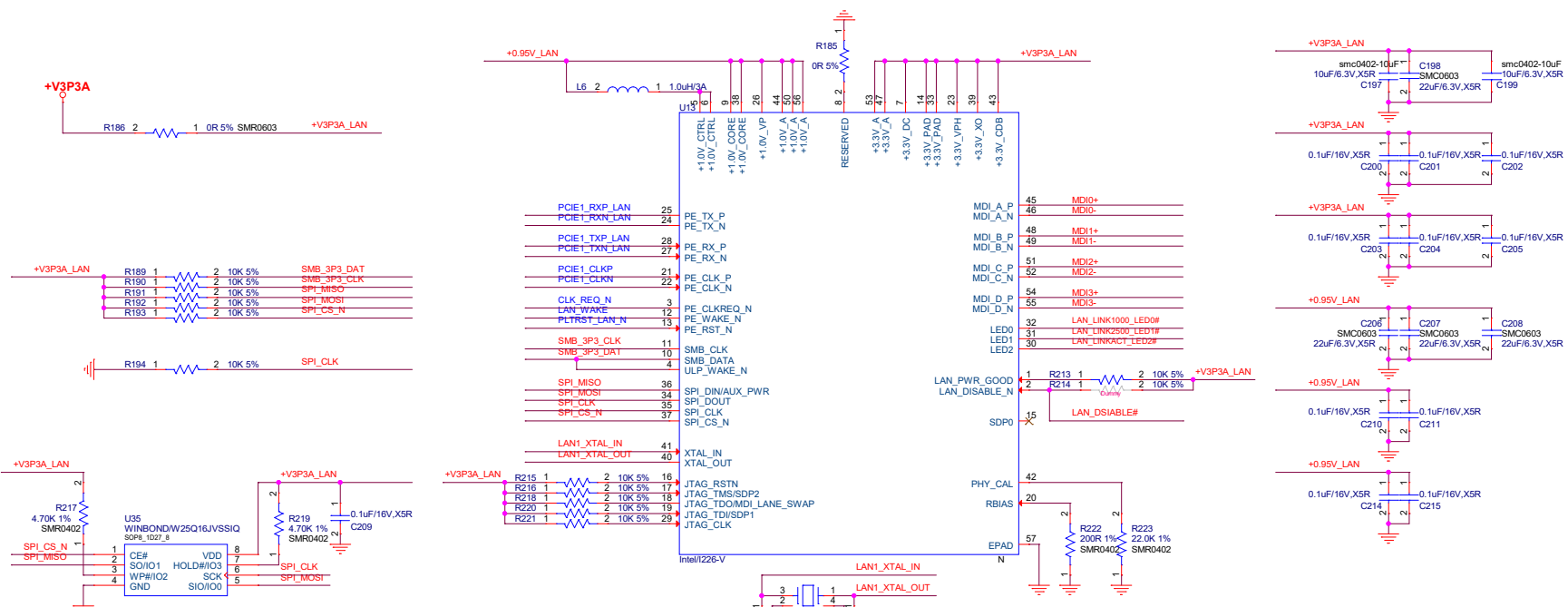




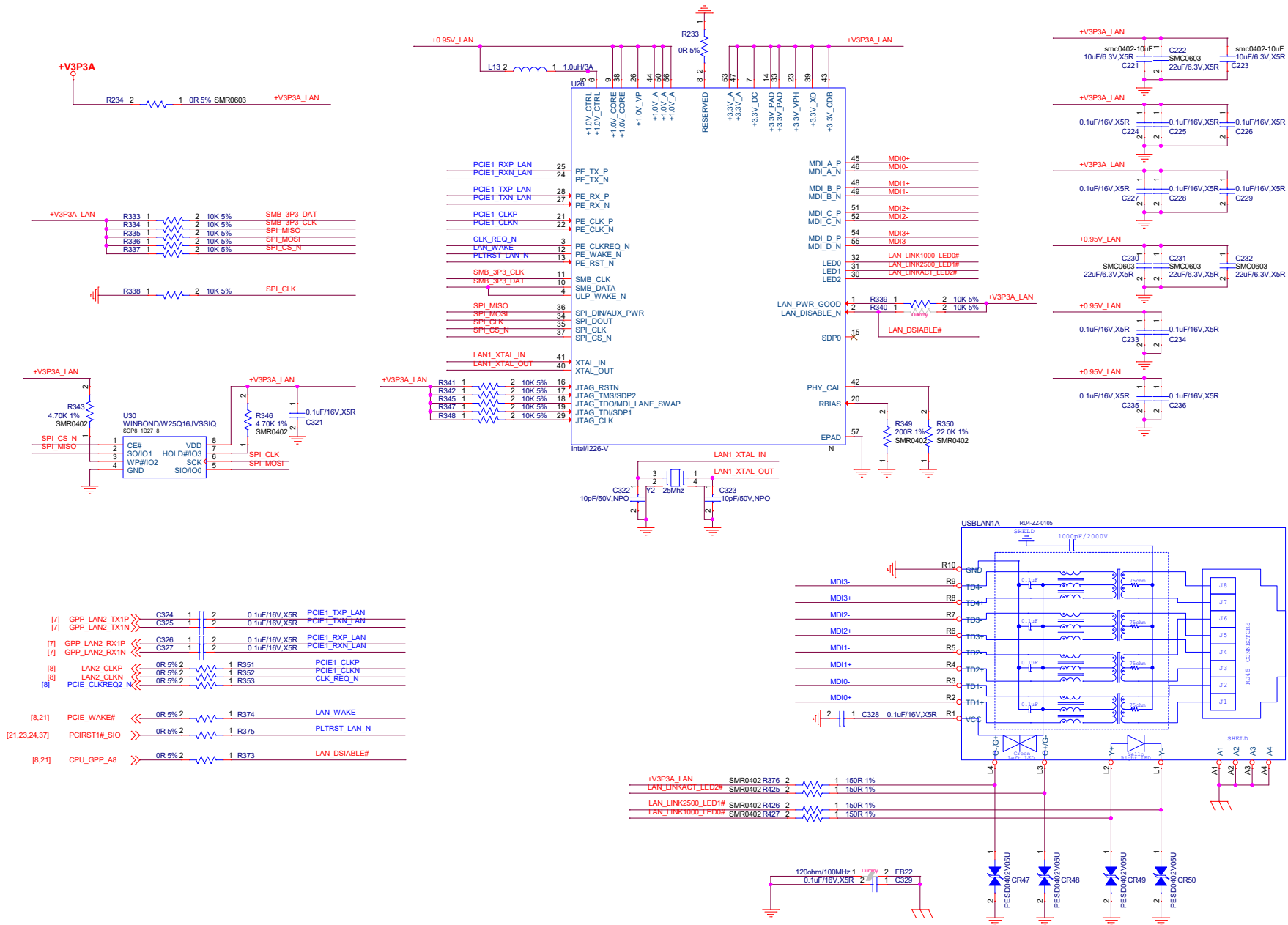
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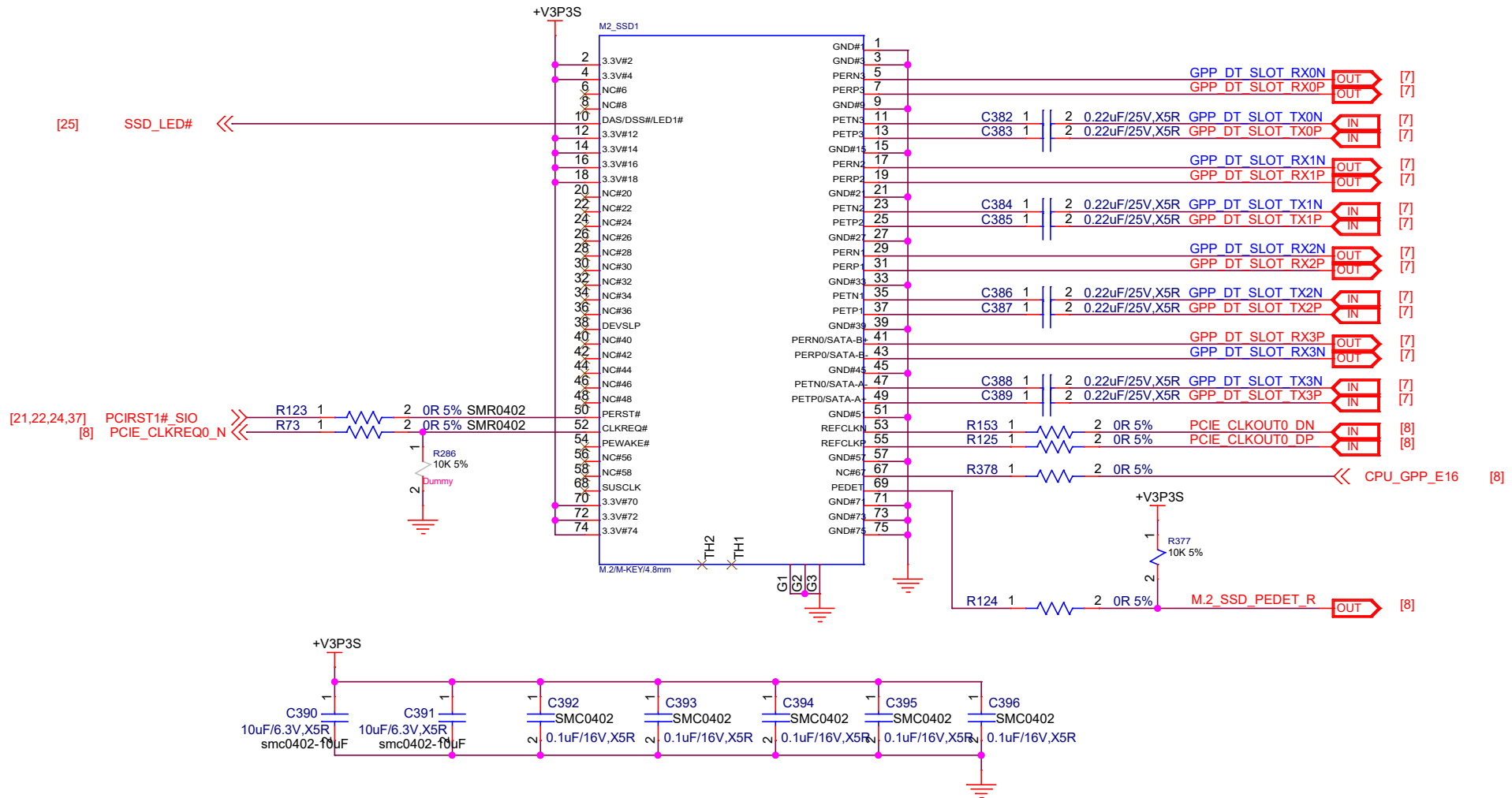
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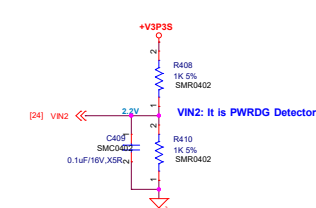
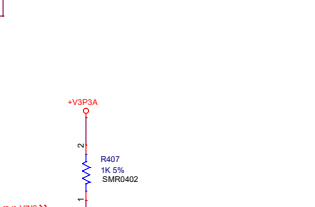
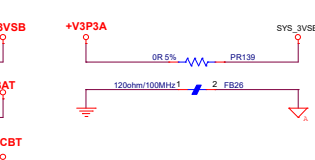
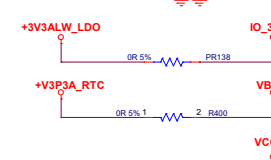
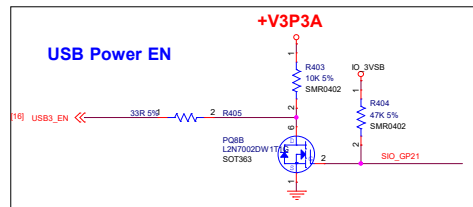
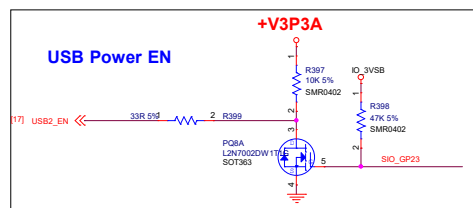
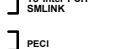
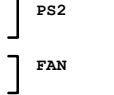
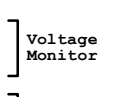
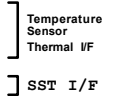
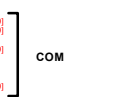
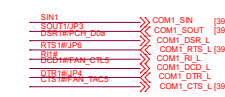
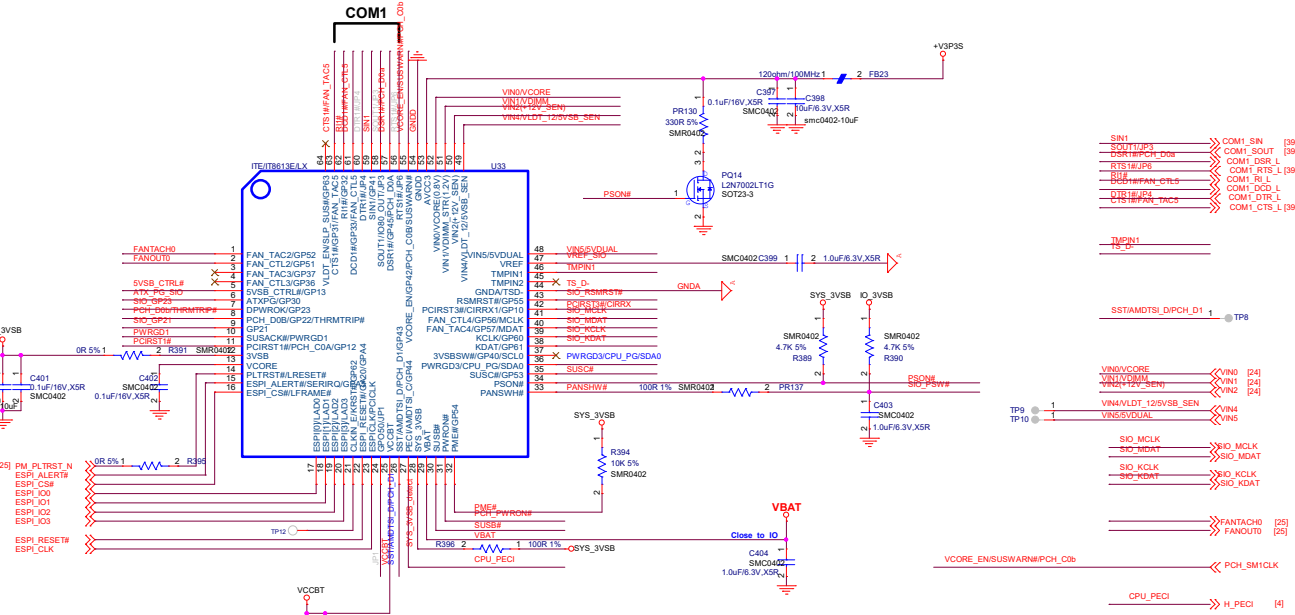
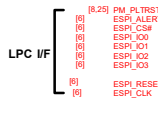
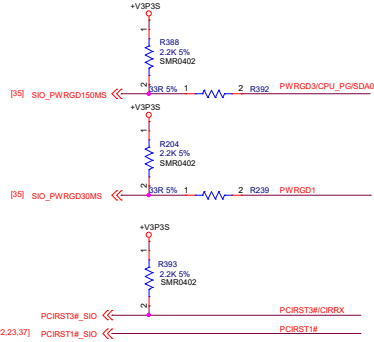
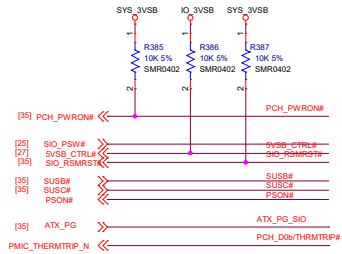
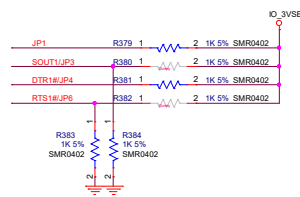
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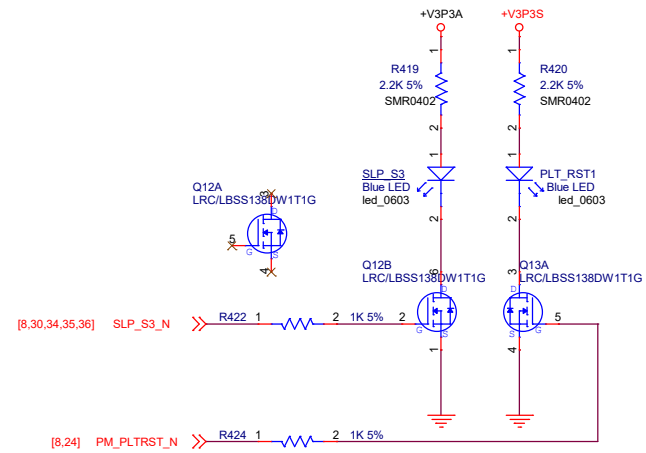
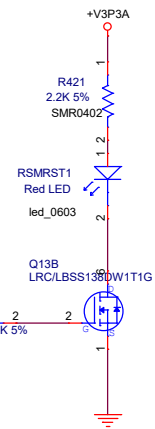
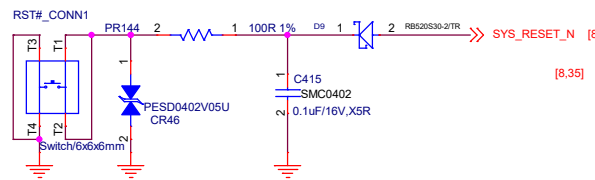
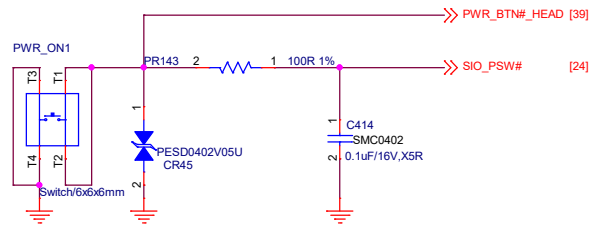
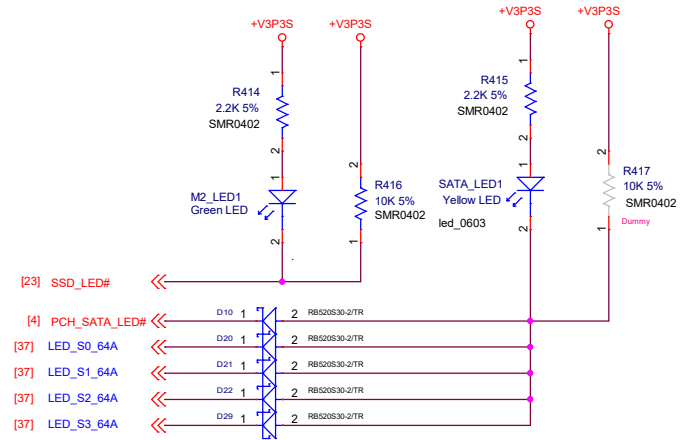
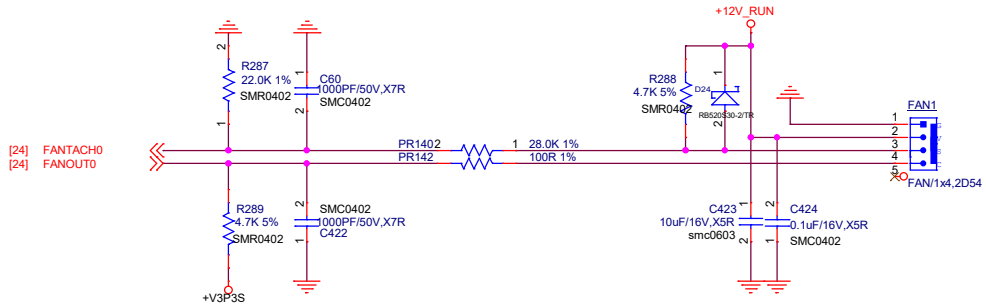


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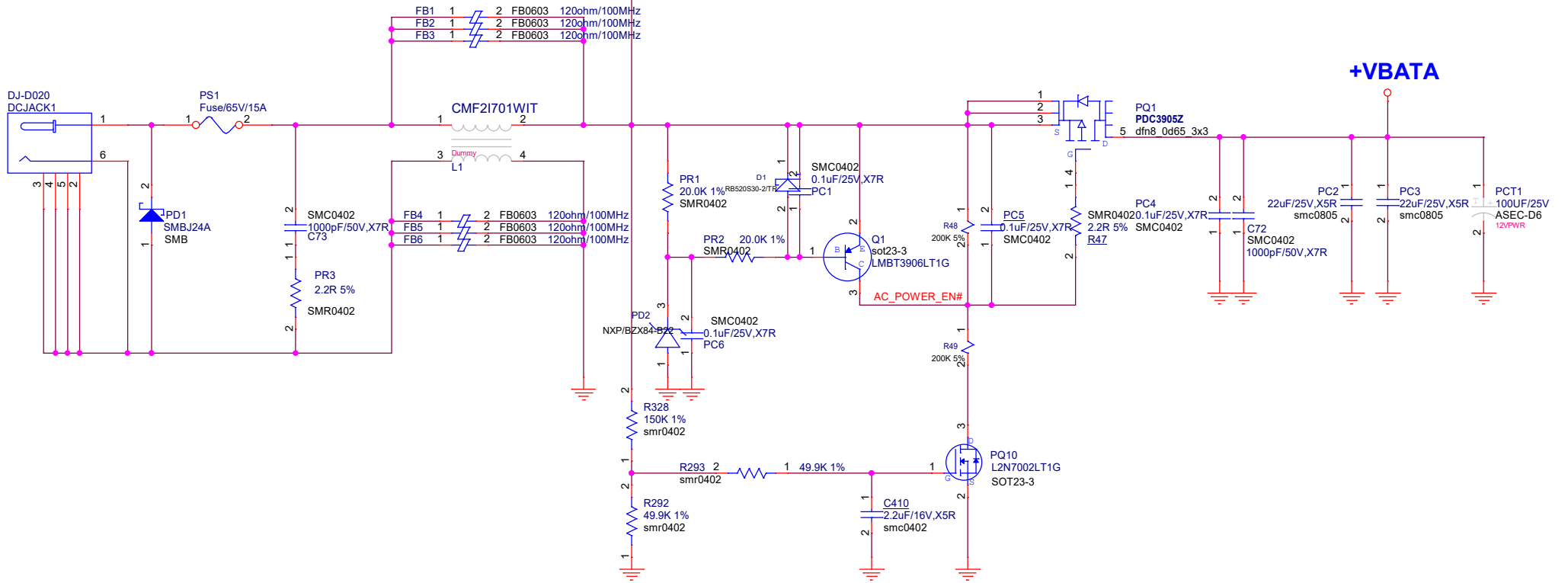
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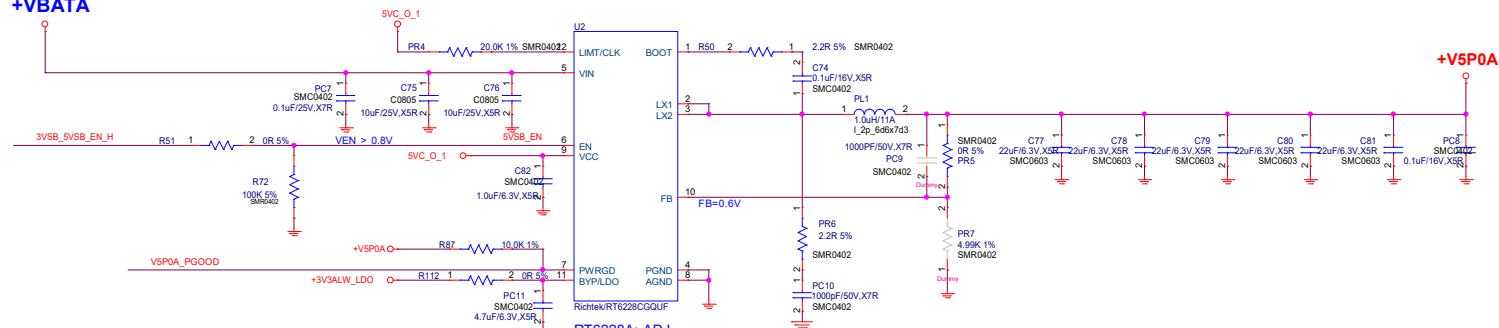
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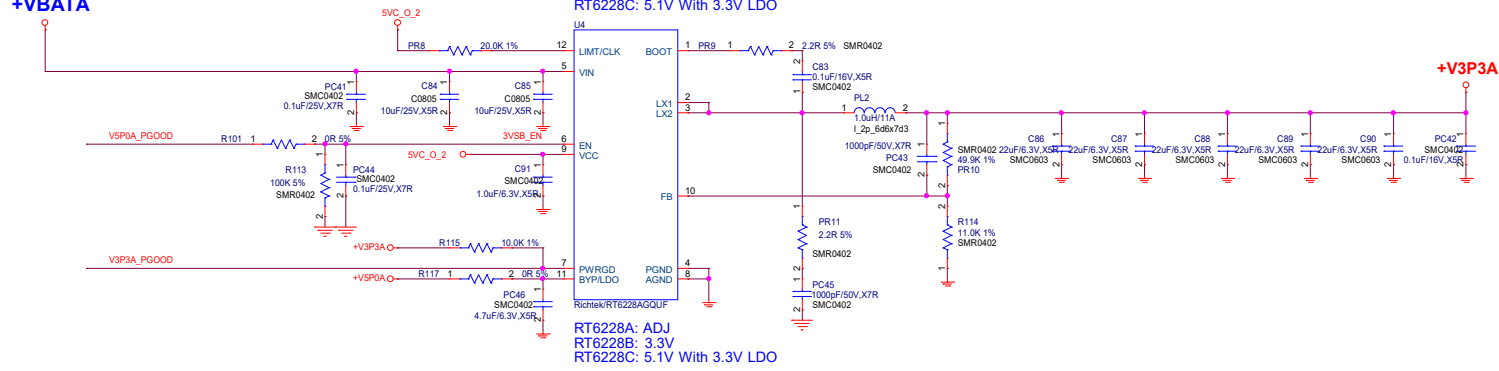


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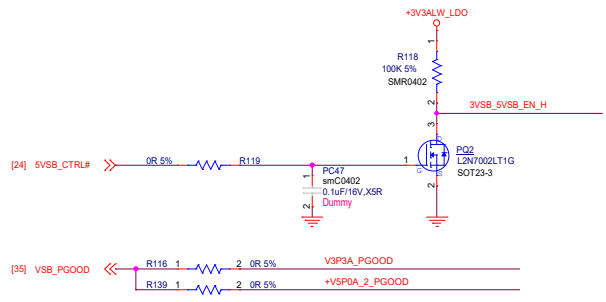
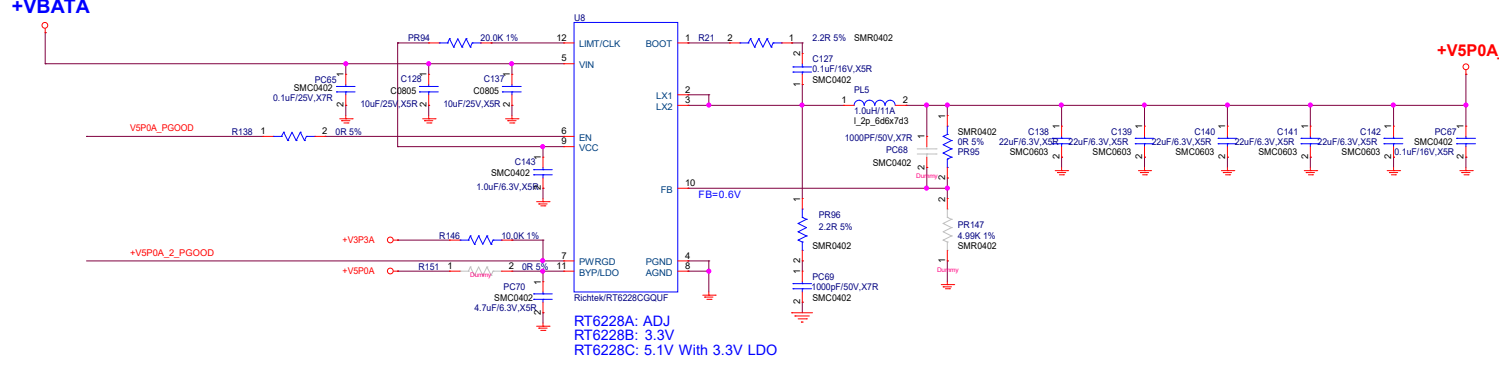
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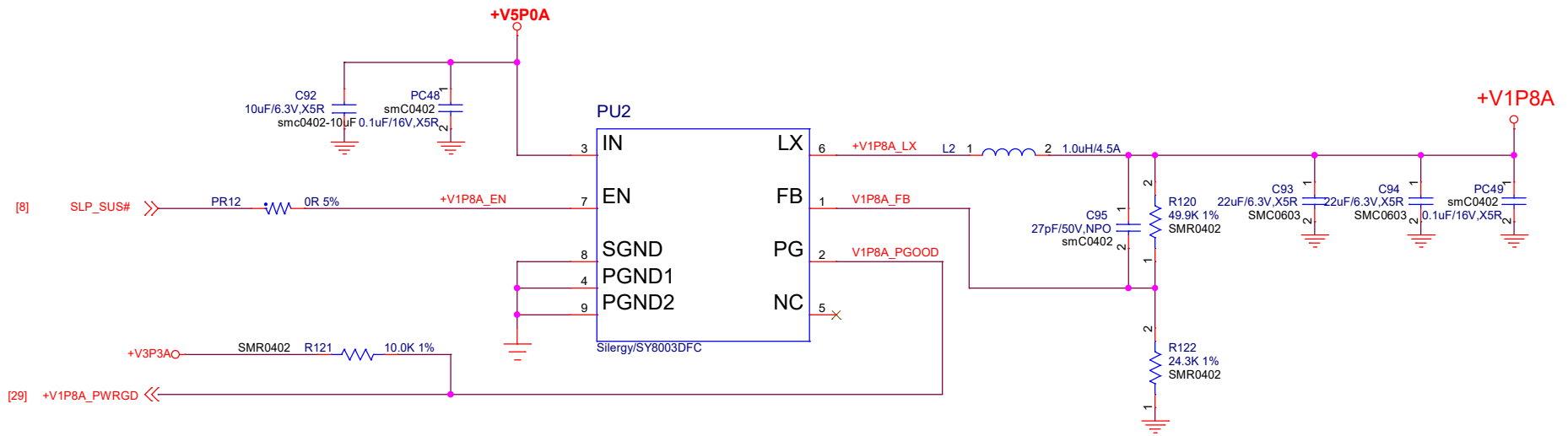
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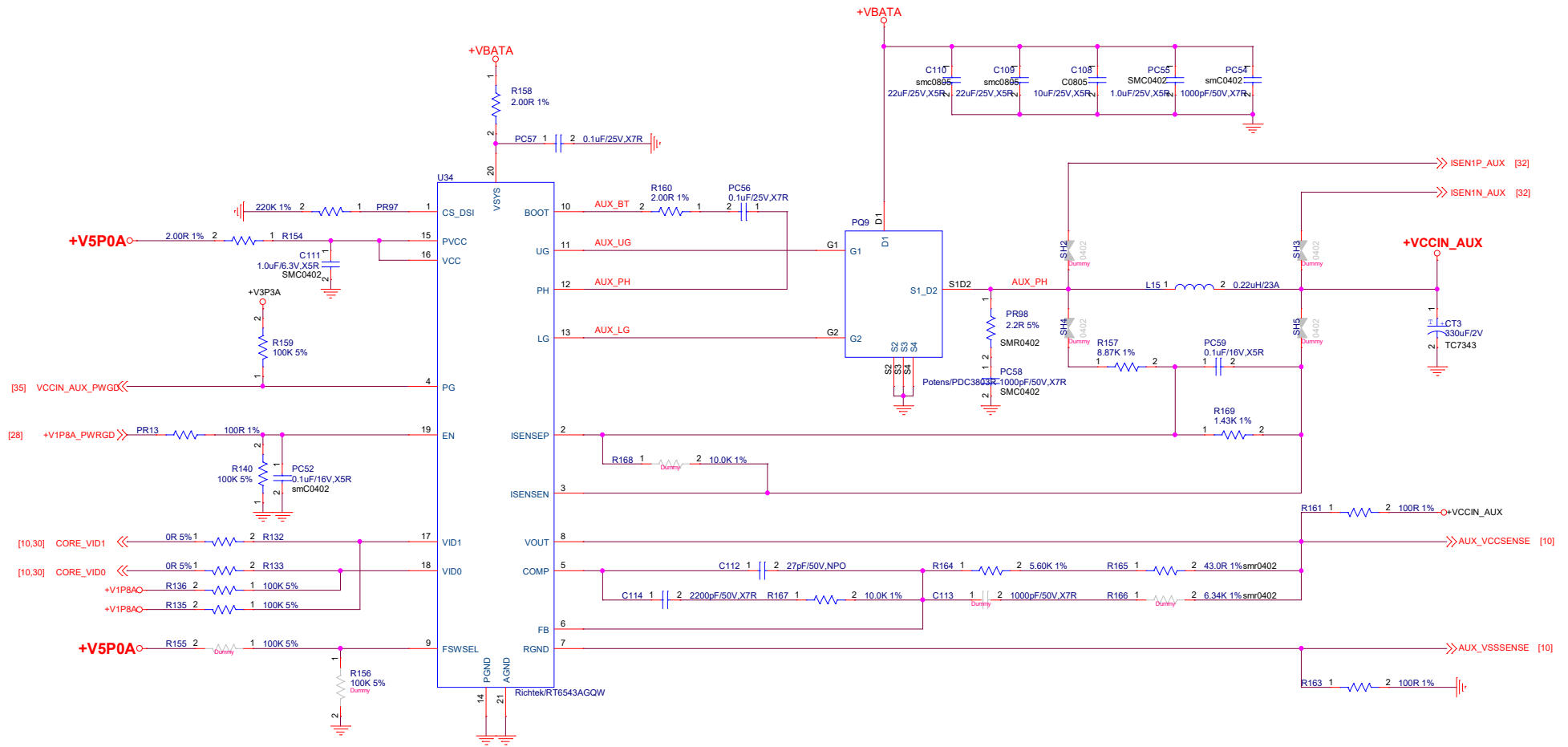
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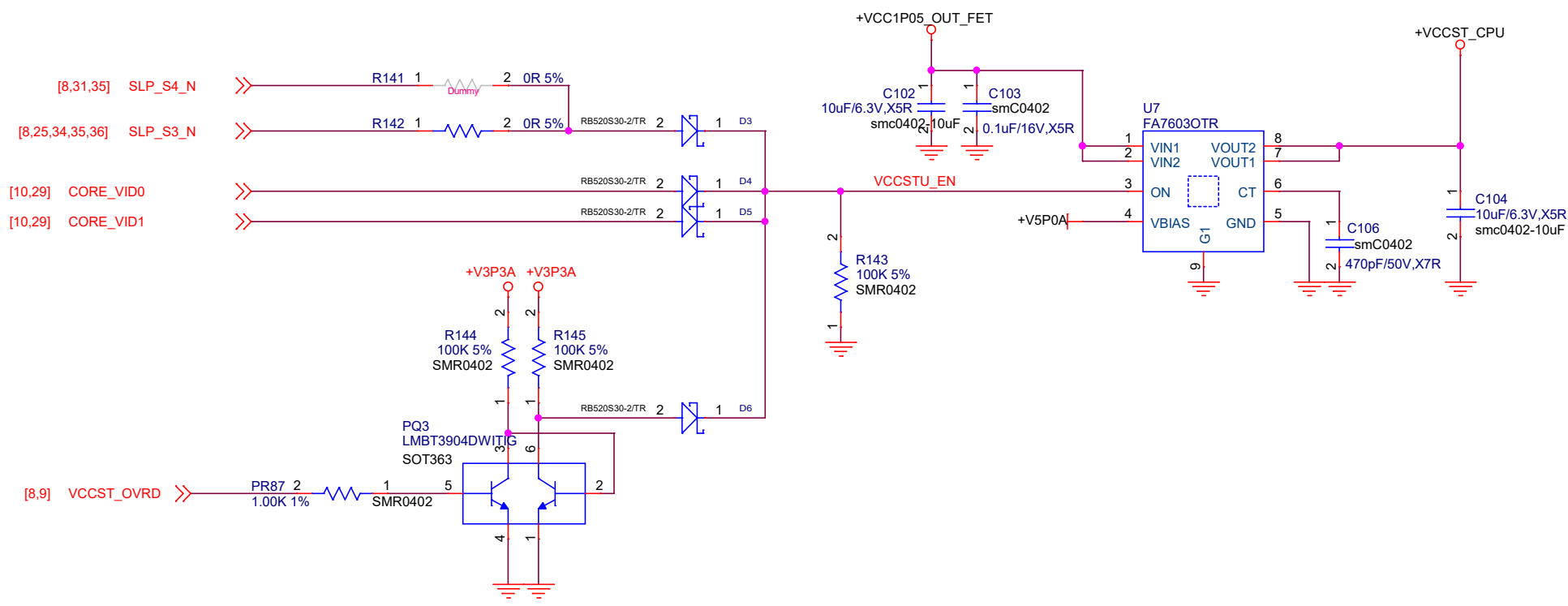


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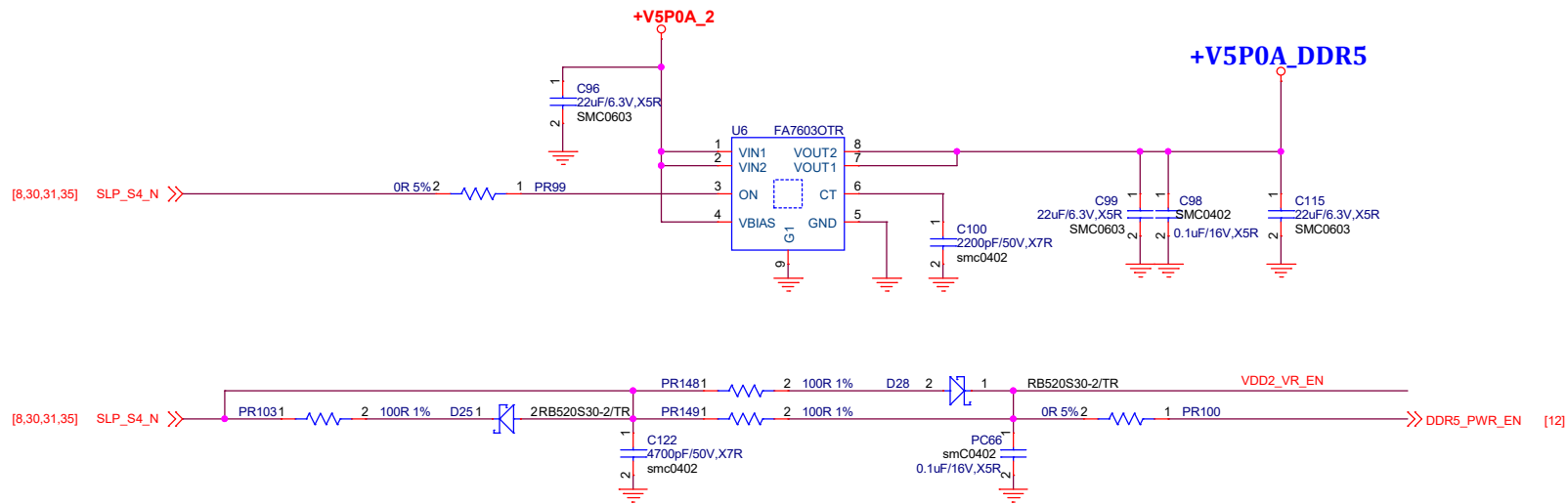
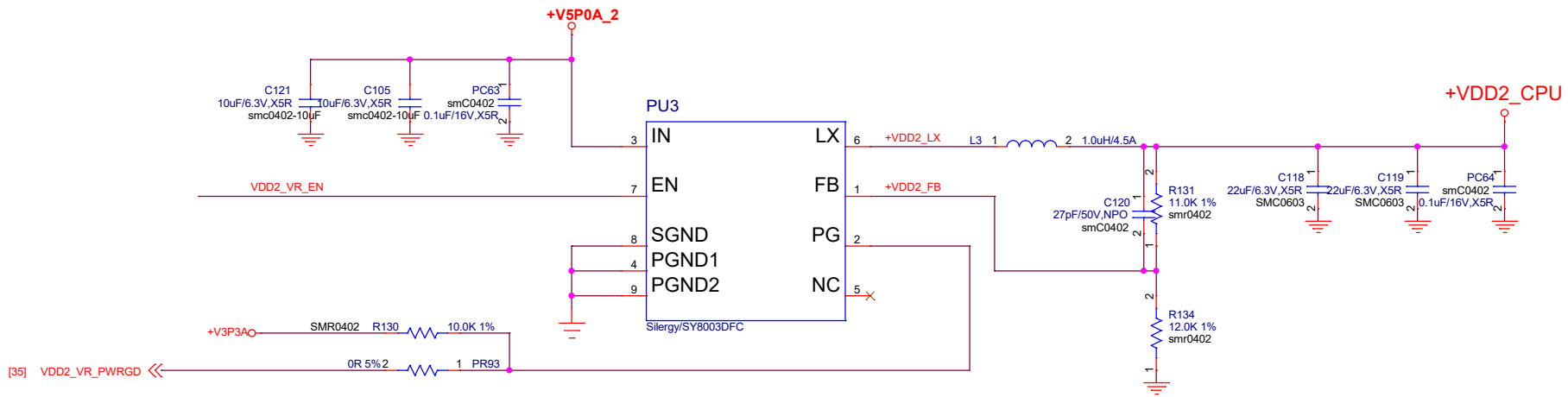


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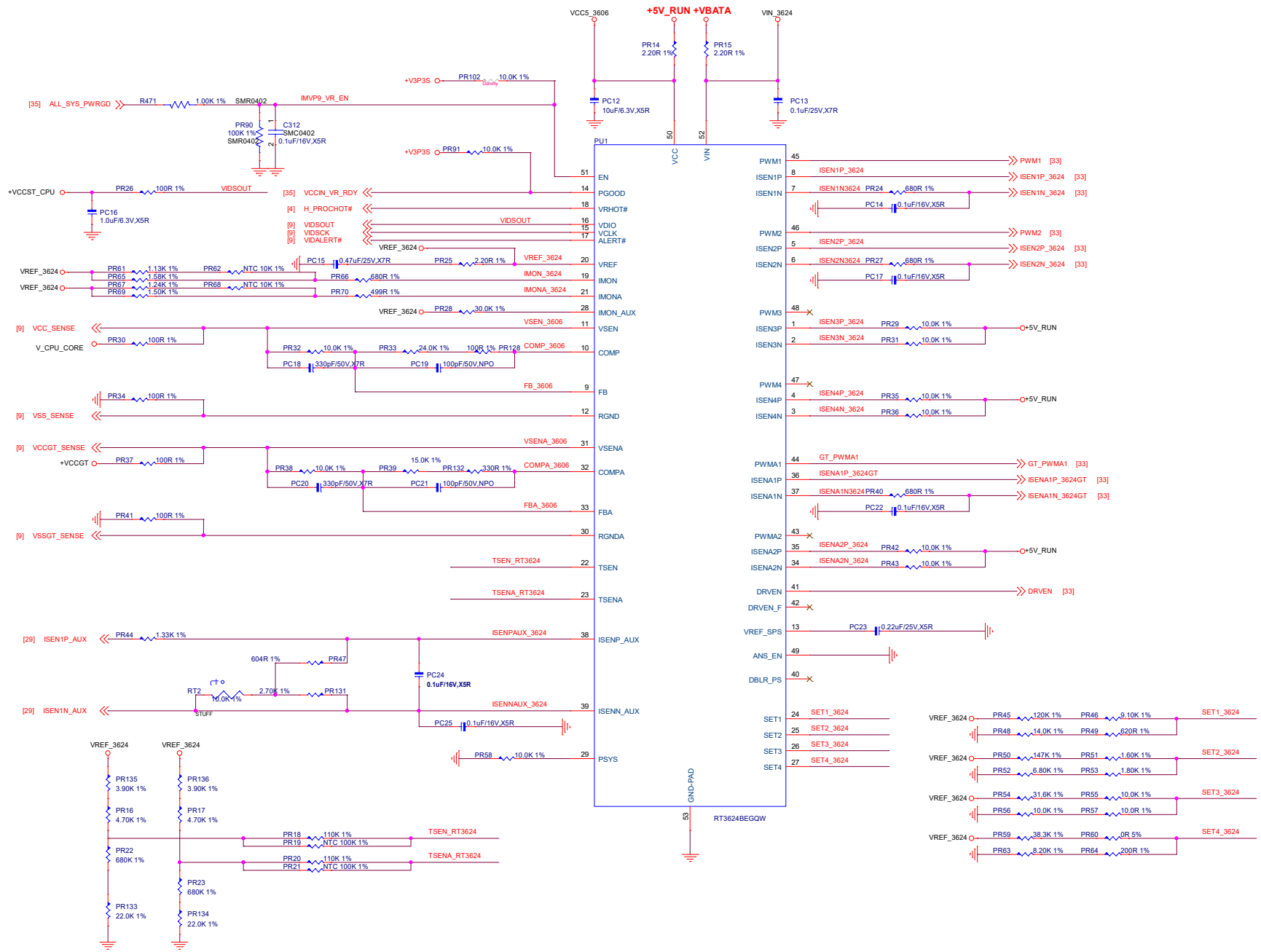




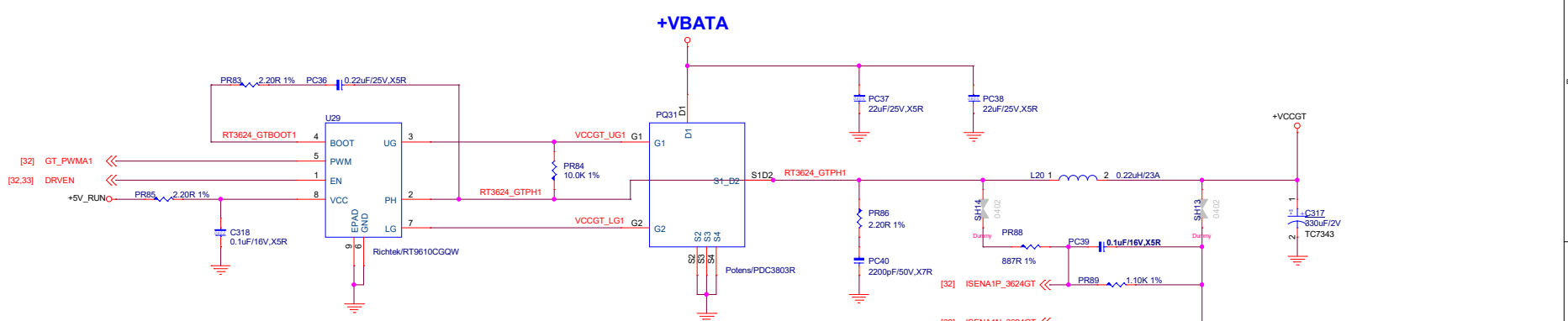
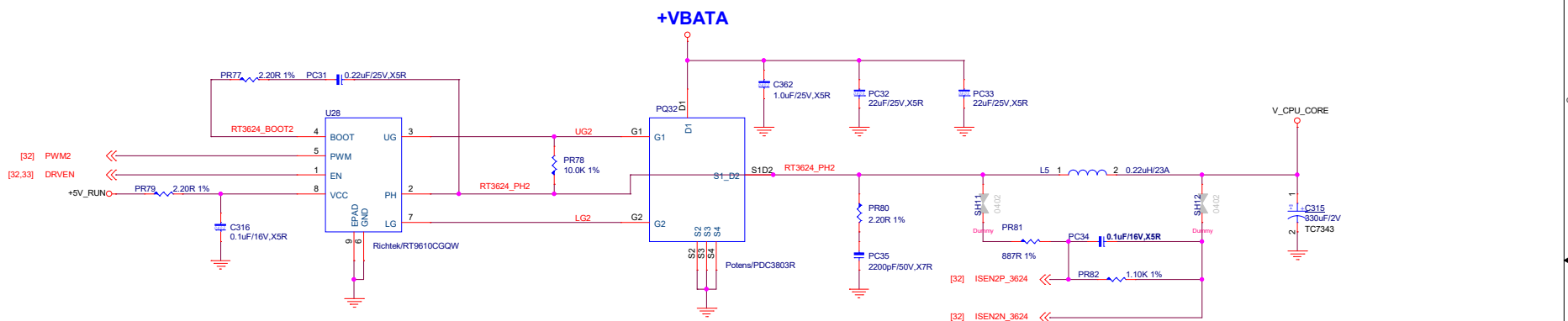
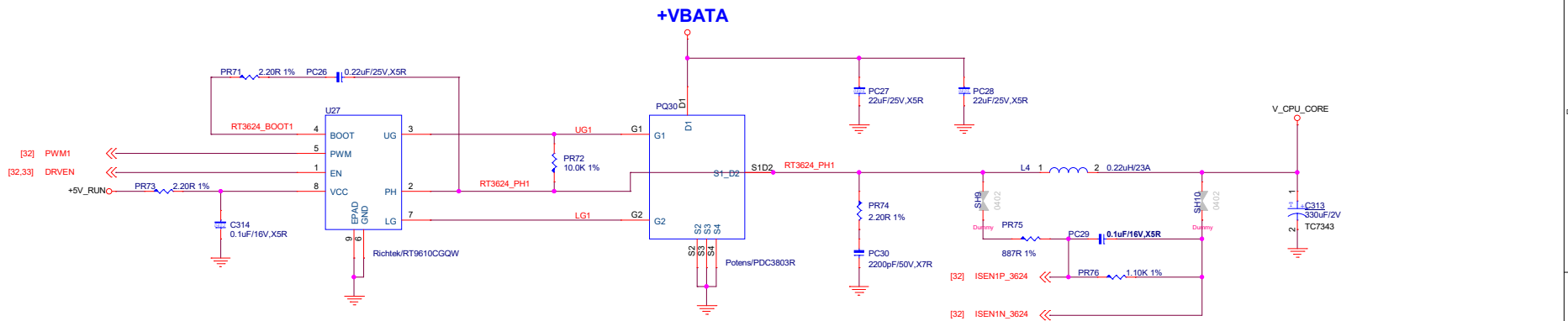
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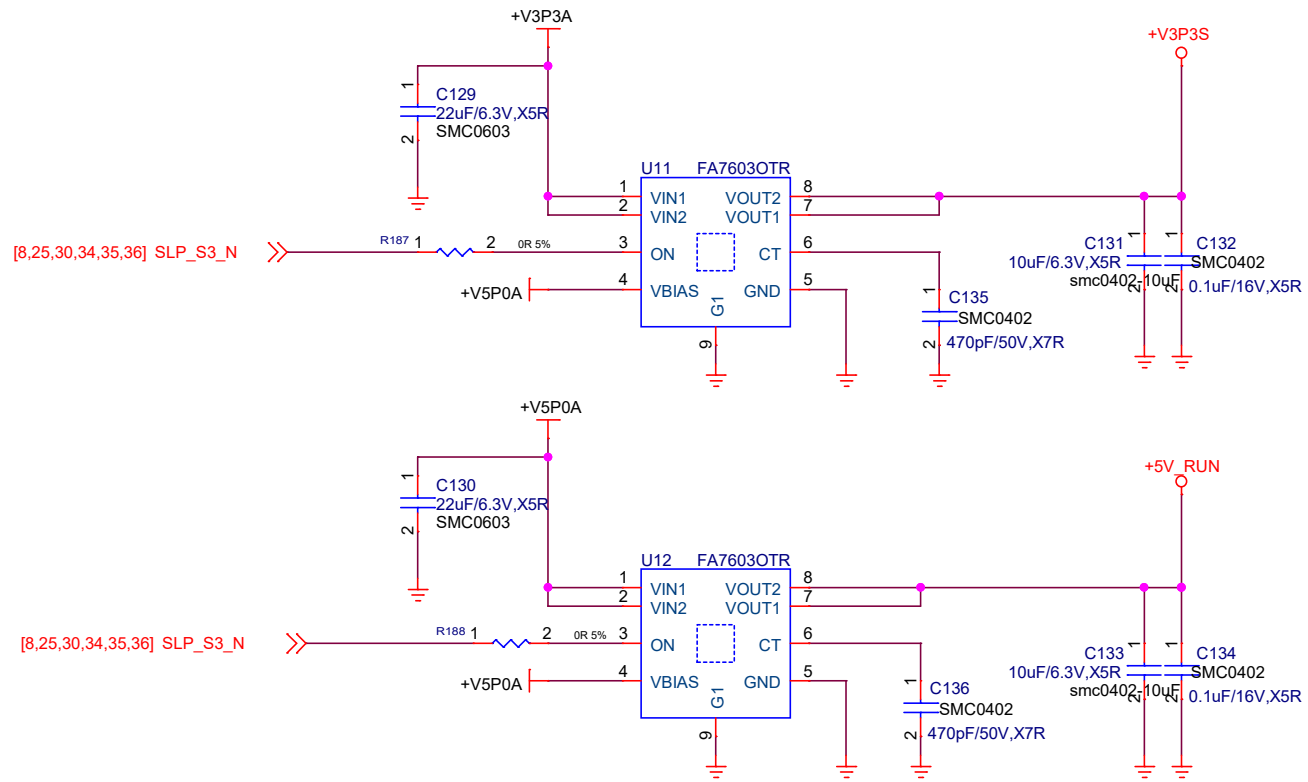


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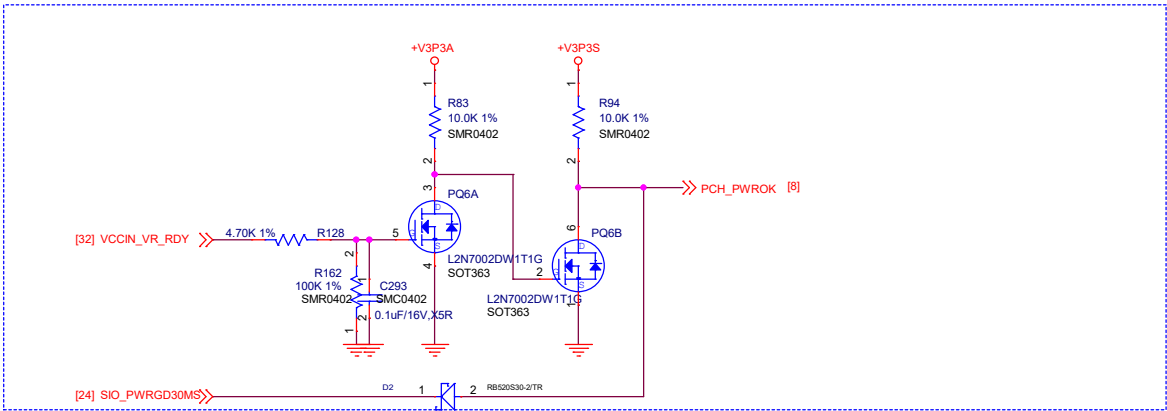
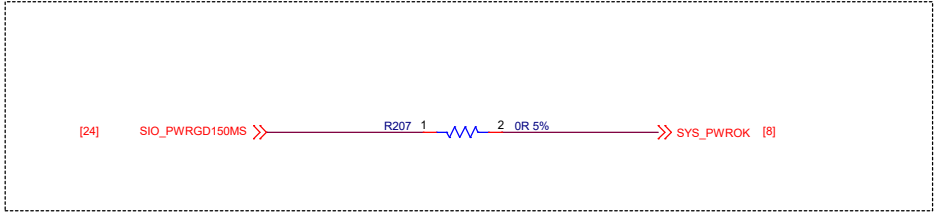
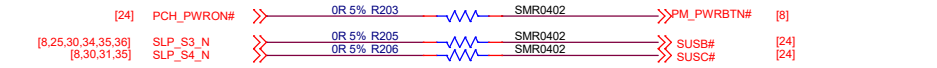
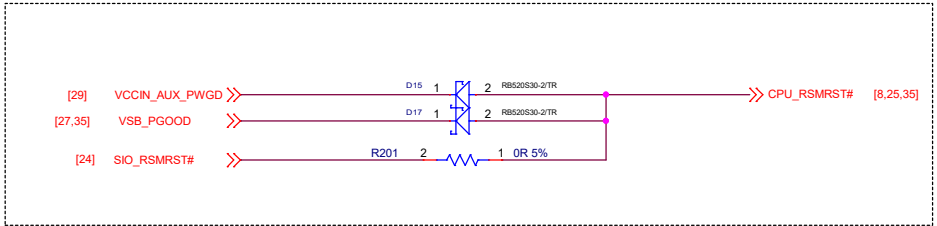
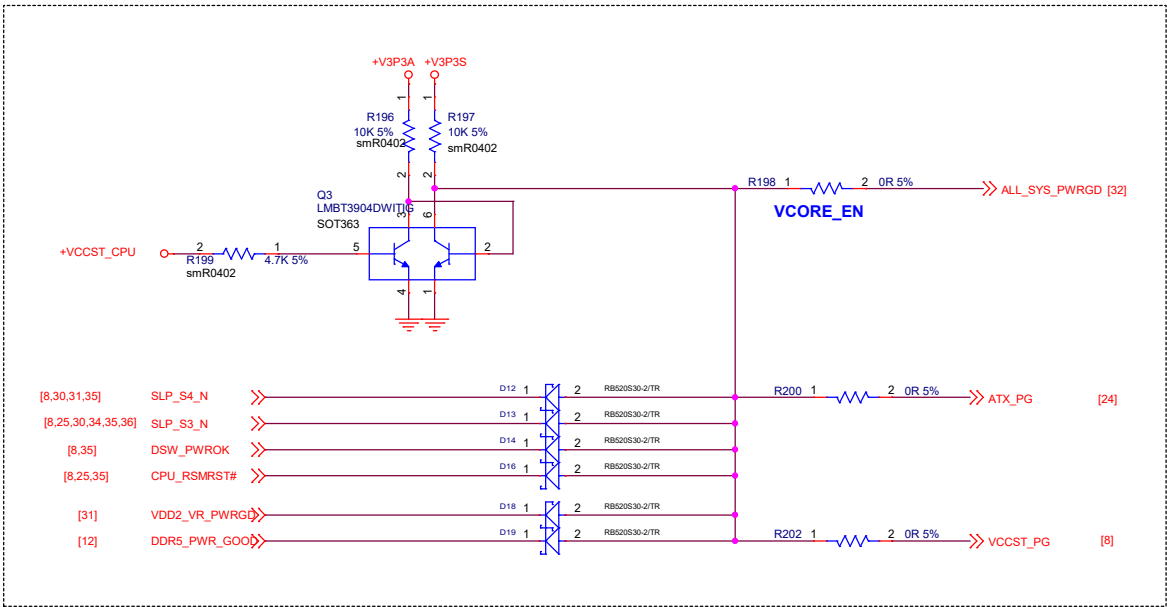
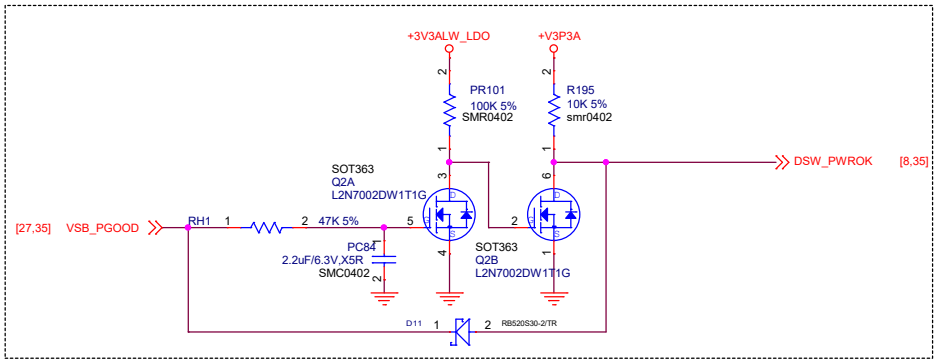


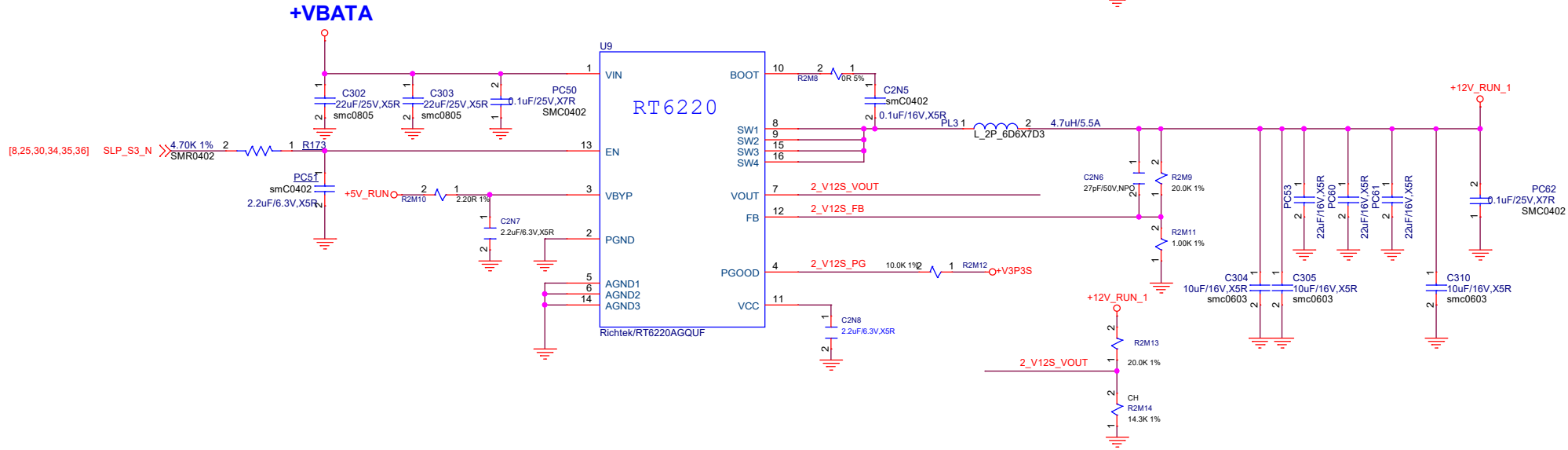
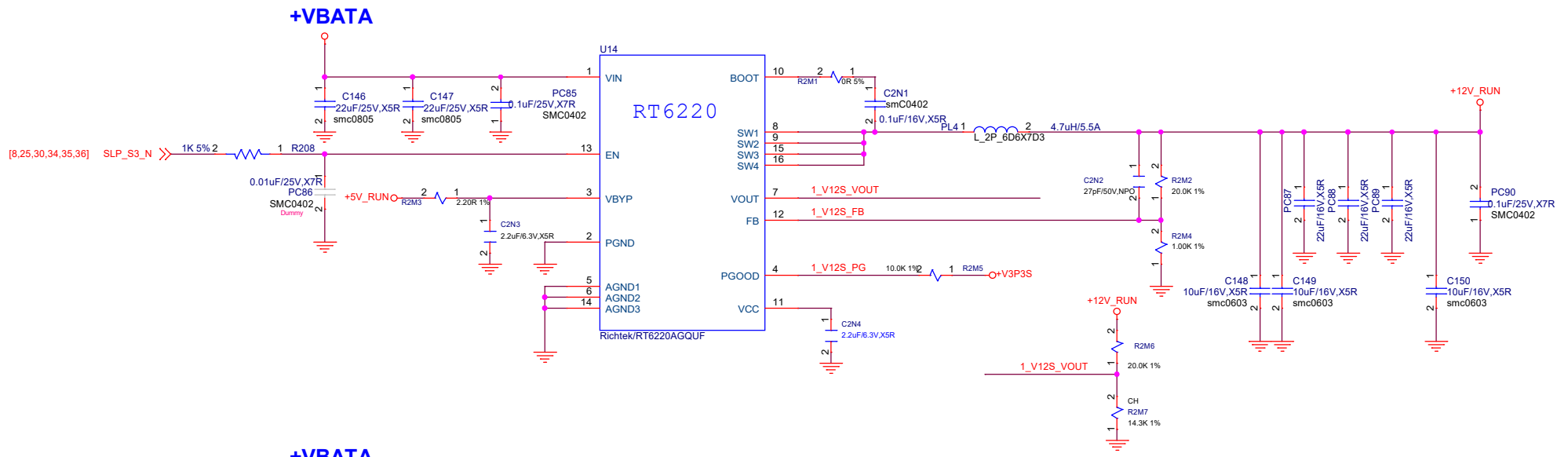
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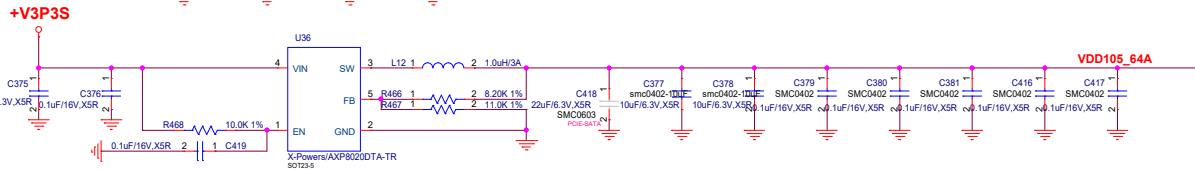
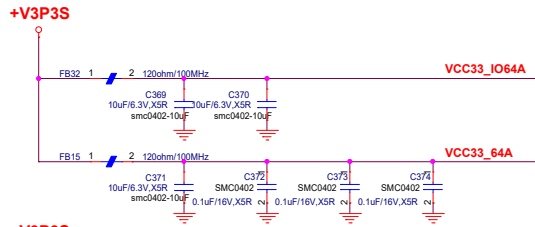
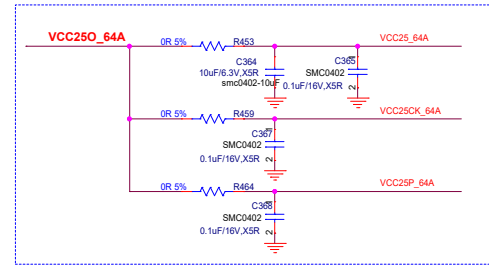
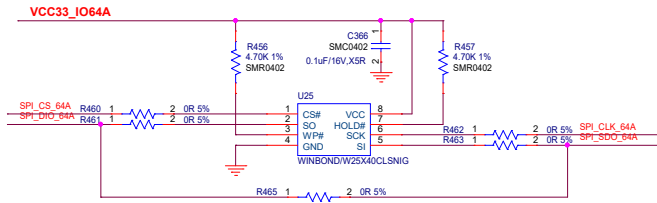
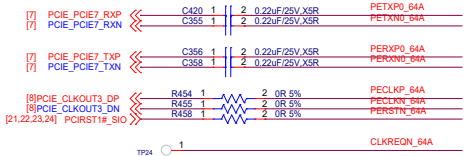
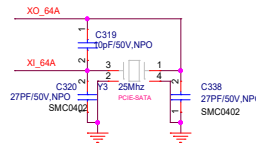
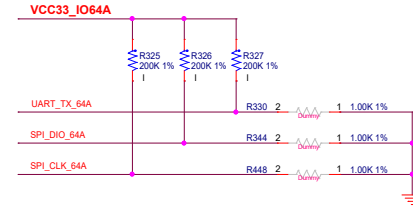
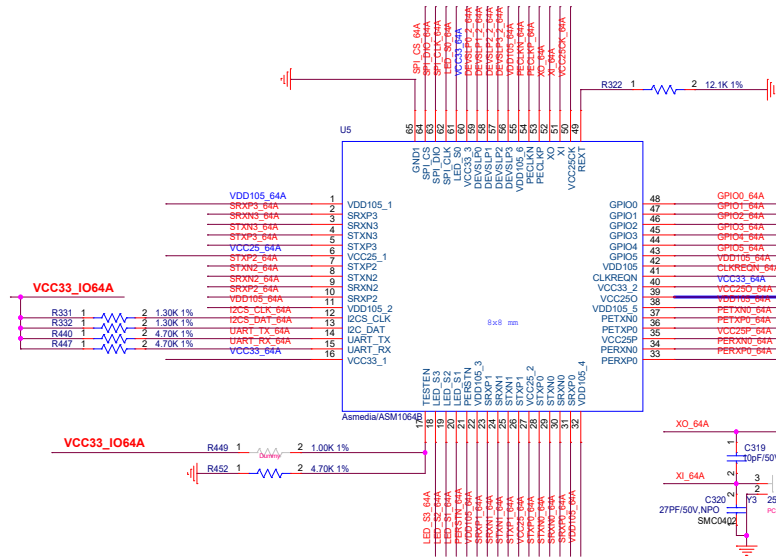
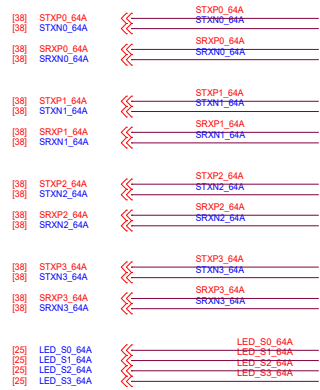


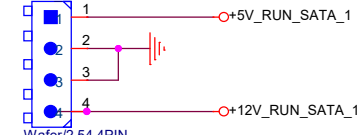
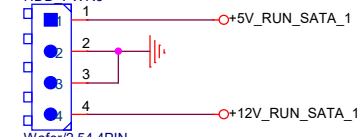
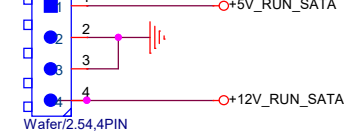
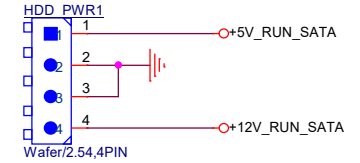
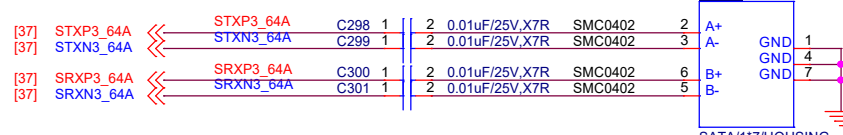
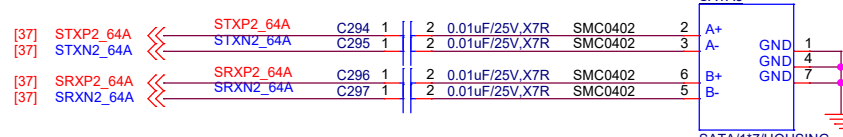
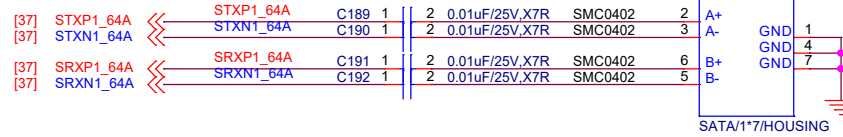
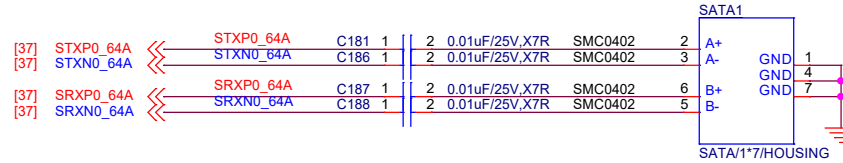
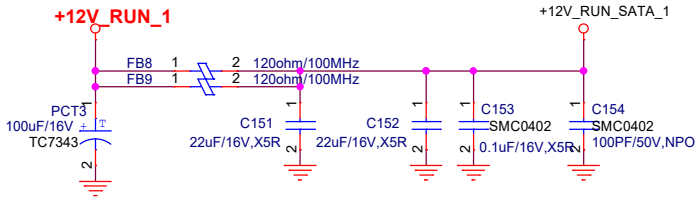
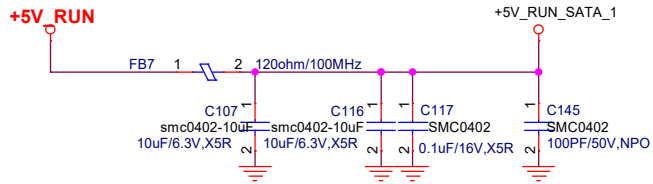
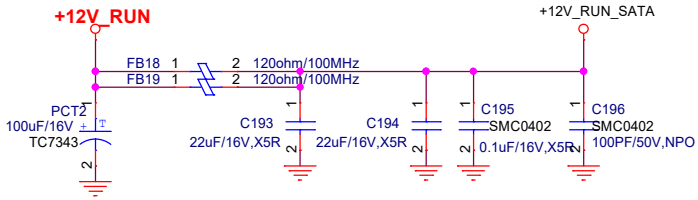
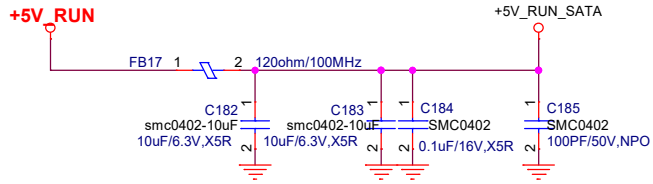
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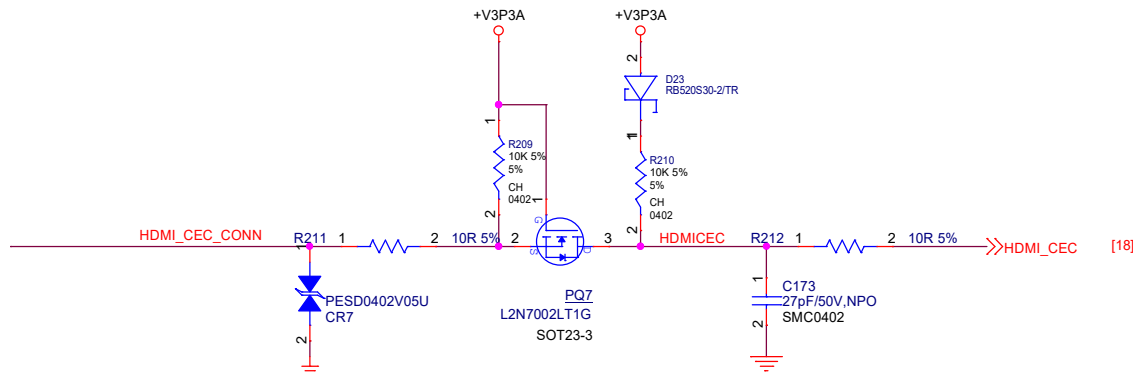
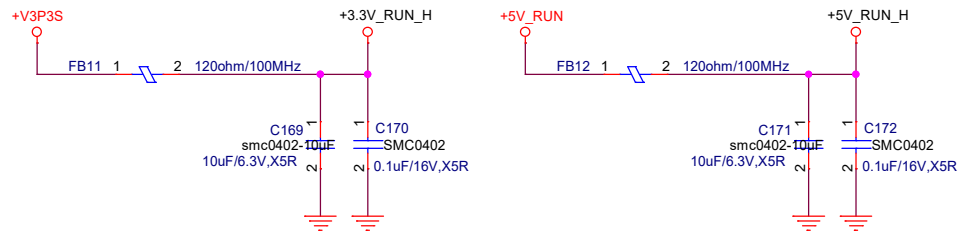
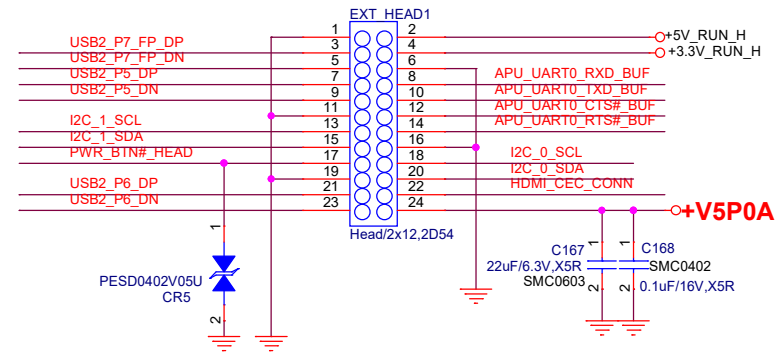
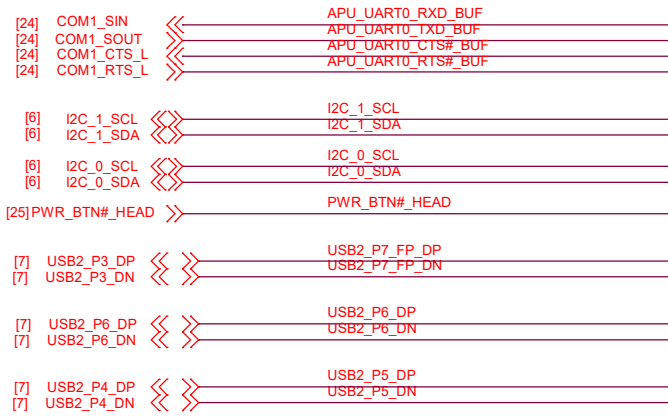


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